

Model Name: H310M S2P 2.0

SHEET TITLE Rev 1.0

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1151-A
05	CPU_LGA1151-B-DDR4
06	CPU_LGA1151-C
07	CPU_LGA1150-D
08	DDR4 CHANNEL A
09	DDR4 CHANNEL B
10	PCH CLOCK/DDC/BIOS
11	PCH_DMI,USB,PCIE
12	PCH_MISC
13	PCH SATA,PCIE,SATA_EXPRESS
14	PCH PWR
15	PCH GND
16	ITE 8686 LPC IO
17	HWM
18	FAN CTRL--SIO
19	Dual BIOS for CS mode
20	CEC Logic (NA)
21	PCI EXPRESS X16 SLOT
22	PCI EXPRESS X1
23	SATA
24	M.2 X2 (P)
25	ISL95866 PWM-IRON
26	ISL95866 MOS_VCORE-IRON
27	ISL95866 MOS_VCCGT-IRON
28	VCCSA_VCCIO_VCCPLL-B.H系列

SHEET TITLE

29	RT8237_DDR_BEAD
30	RT8068A_VPP
31	RT8237_PCH-BEAD
32	DISCRETE POWER
33	ATX POWER , A_-PROCHOT
34	KB_MS_USB
35	RT2168 - DP to VGA - IC
36	RT2168 - DP to VGA - Conn
37	REALTEK RTL8118
38	USB_LAN CONNECTOR-8118
39	Realtek ALC887
40	REAR AUDIO JACK
41	Audio LED
42	IT8892E/JX
43	PCI SLOT
44	DVI CONN
45	F_USB30
46	HDMI , R_USB , F_USB
47	F_PANEL
48	COM, LPT, TPM , TPM2.0 , RI
49	EMI-ESD
50	POWER MAP
51	NTC MAP
55	POWER MAP
56	NTC MAP

rev1.0

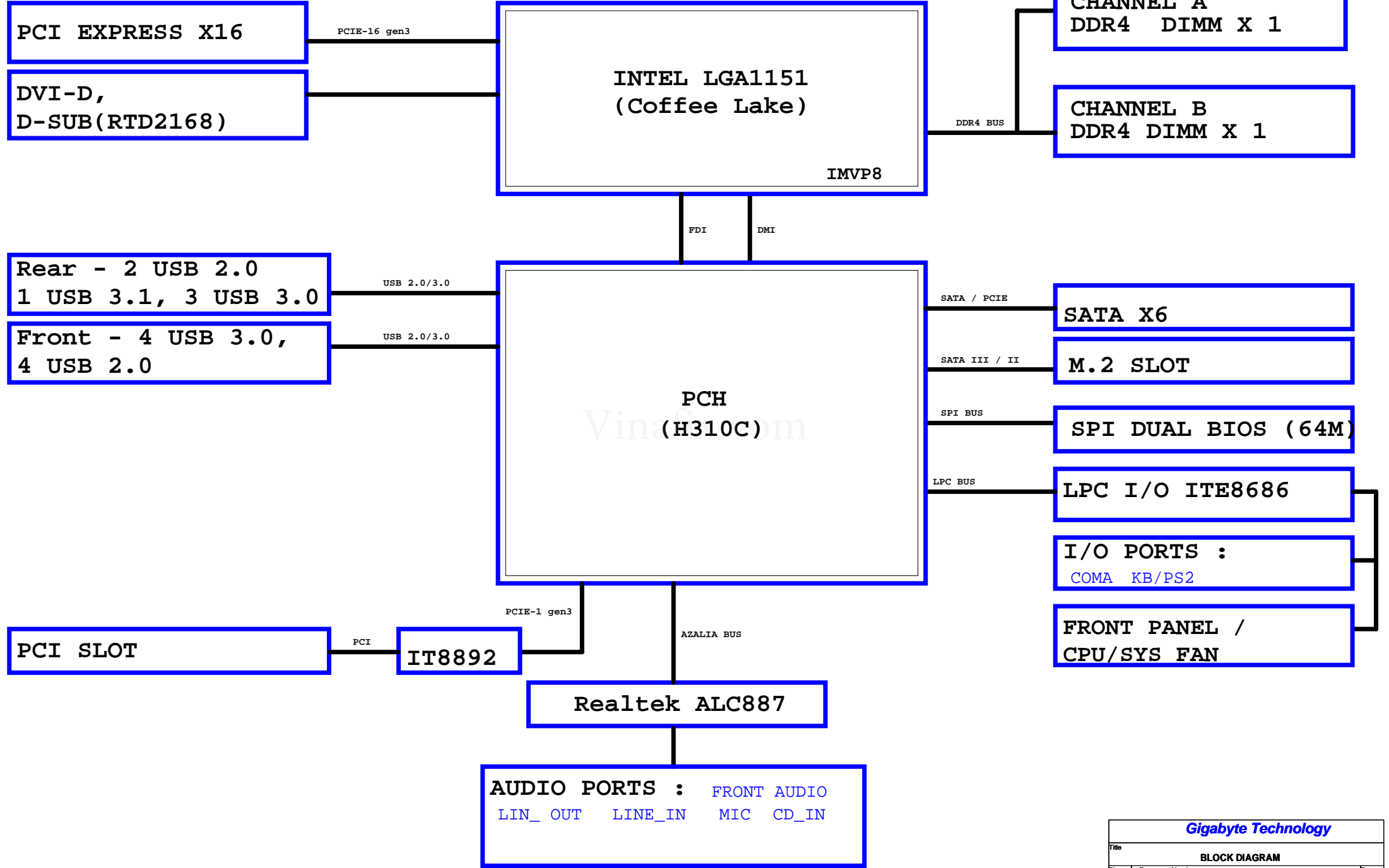
Circuit or PCB layout change

Component value change history

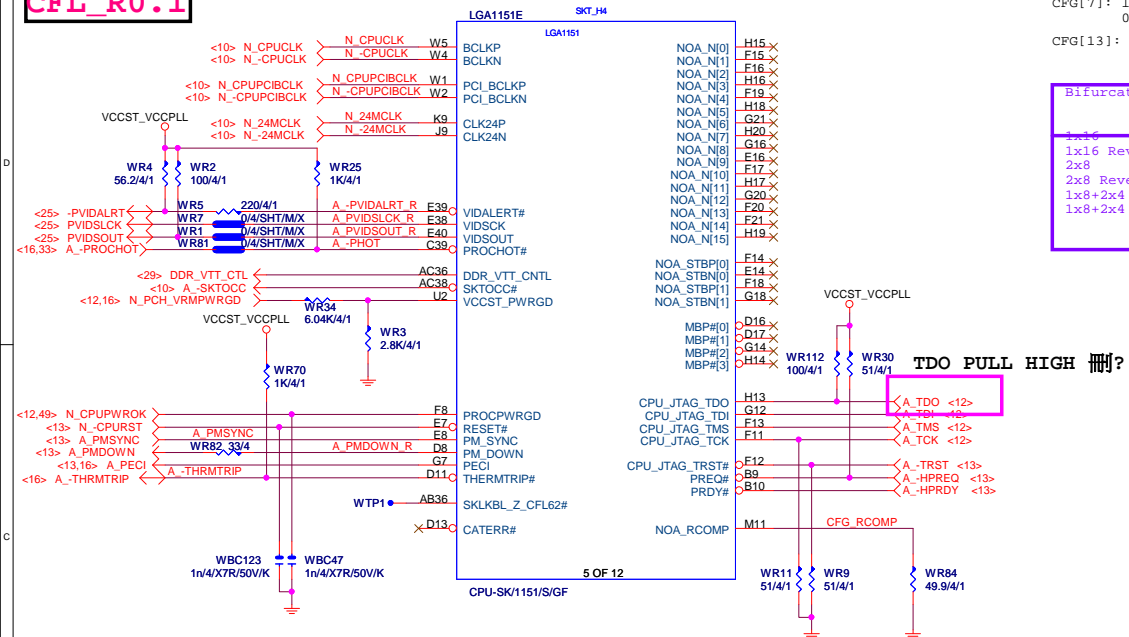
2017/07/19

[illegible][illegible]

BLOCK DIAGRAM

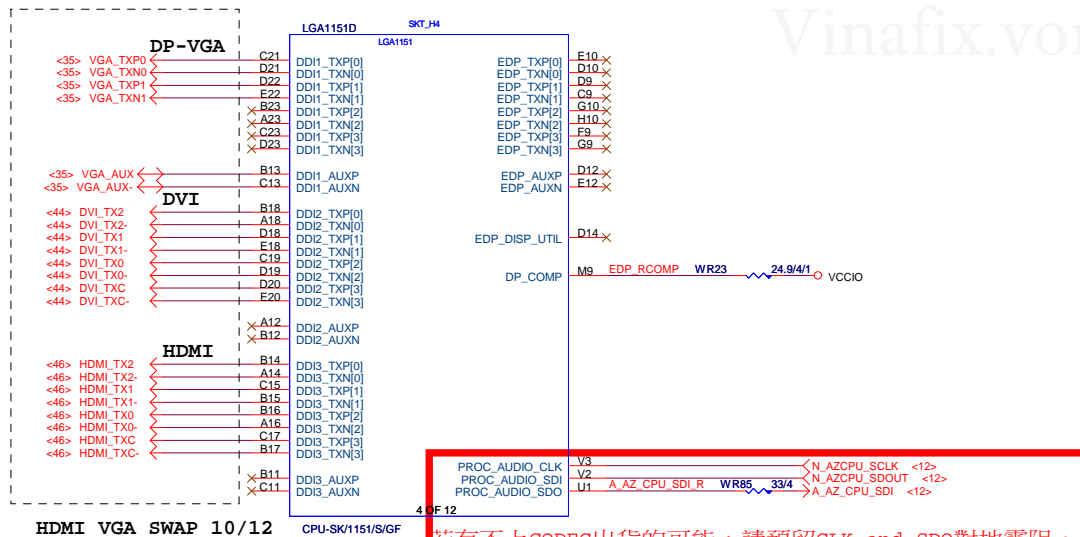
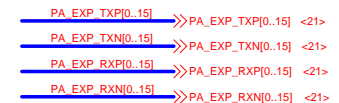
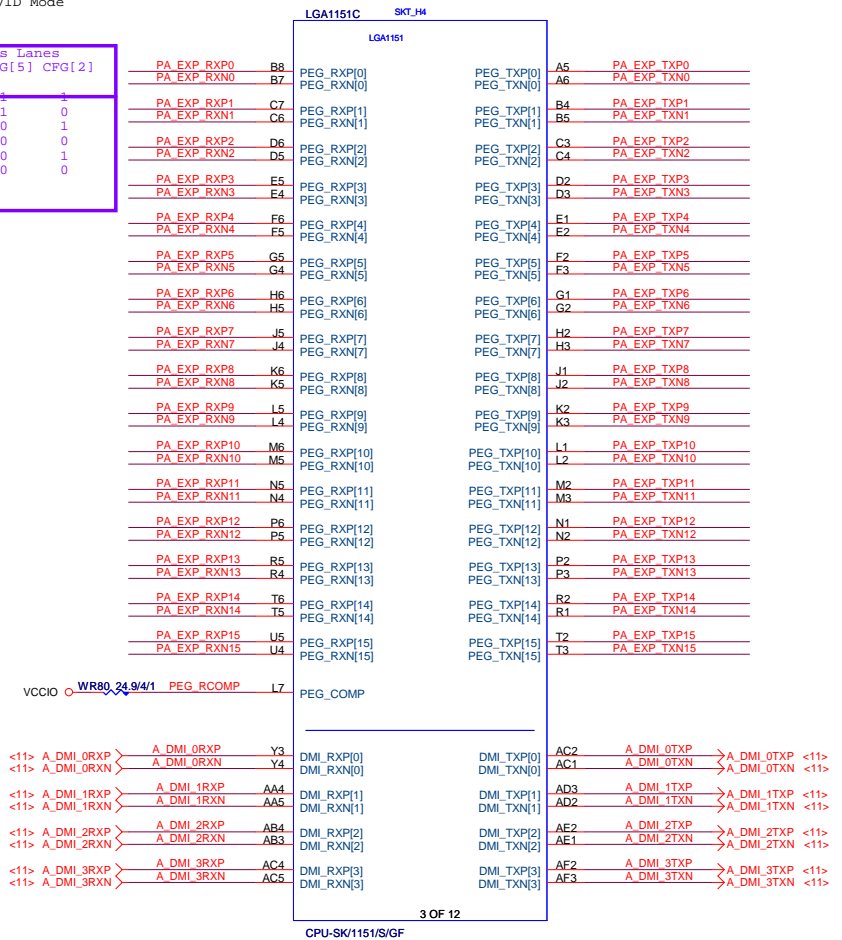


CFL_R0.1



```
CFG[4]: 1=eDP enable / 0=eDP disable
CFG[7]: 1=PEG Train immediately following RESET
        0=PEG Wait for BIOS
CFG[13]: 1=VCCSA Fixed Mode / 0=SVID Mode
```

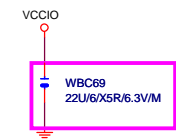
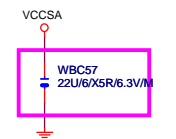
Bifurcation Config.	Signals Lanes		
	CFG[6]	CFG[5]	CFG[2]
1x16	1	1	1
1x16 Reversed	1	1	0
2x8	1	0	1
2x8 Reversed	1	0	0
1x8+2x4	0	0	1
1x8+2x4 Reversed	0	0	0



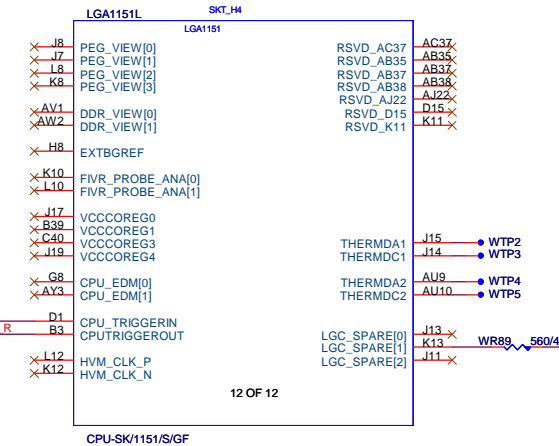
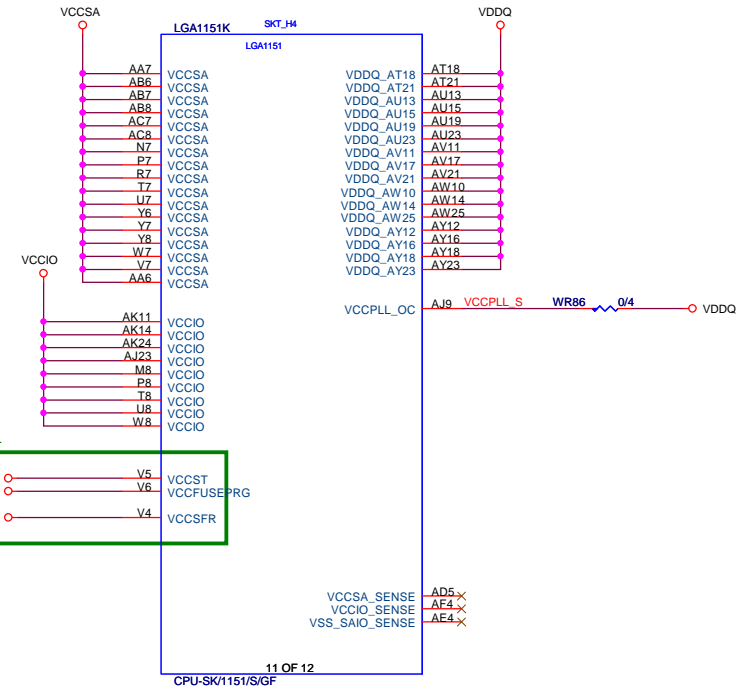
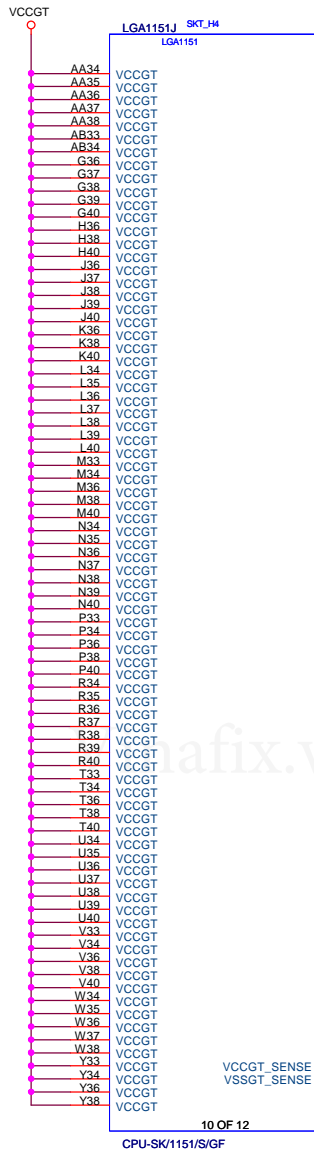
若有不上CODEC出貨的可能，請預留CLK and SDO對地電阻。

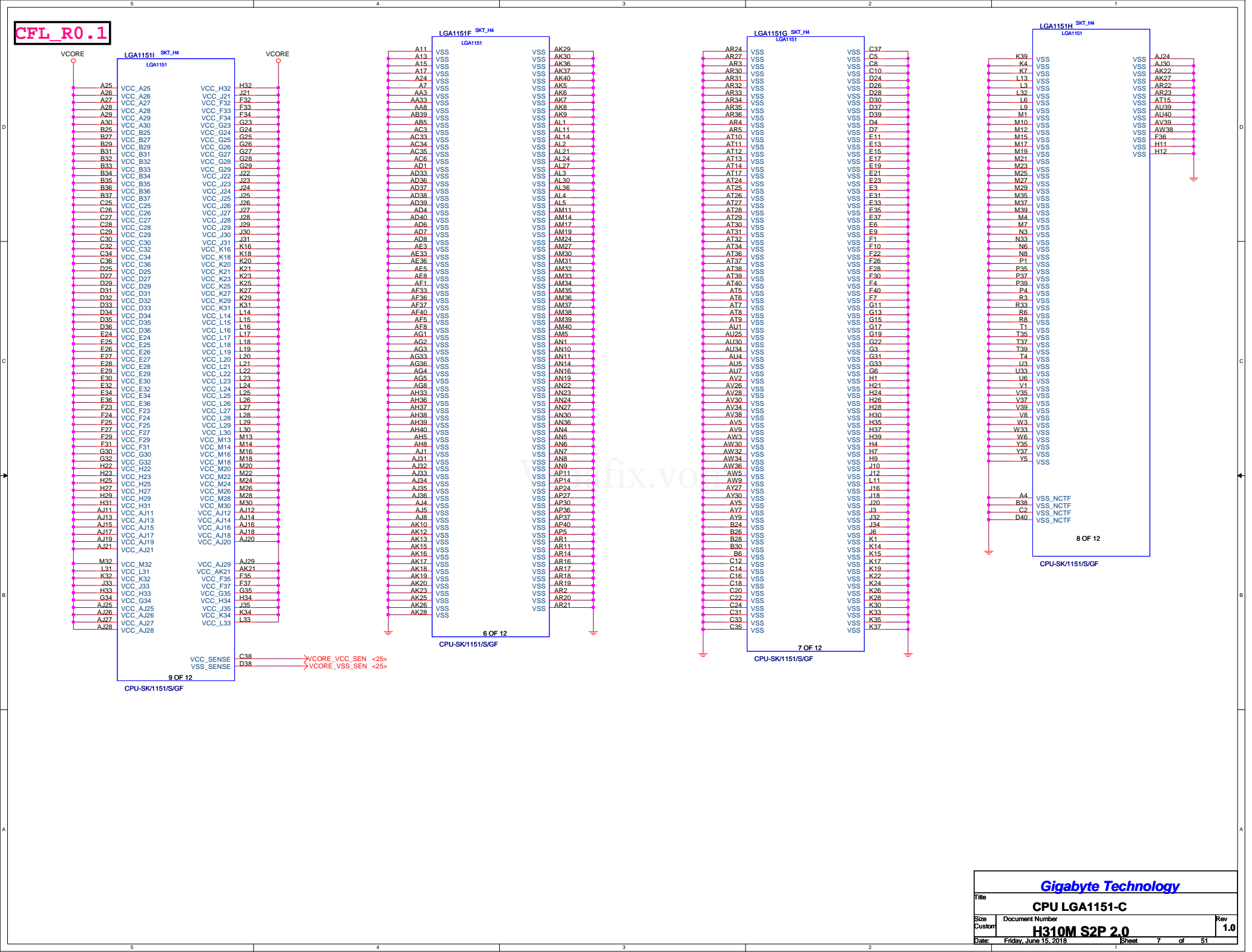
G-15u : (CPU-SK/1151/S/15)
10SC1-F01151-11R / 10SC1-F01151-12R
G-FL : (CPU-SK/1151/S/GF)
10SC1-F01151-21R / 10SC1-F01151-22R

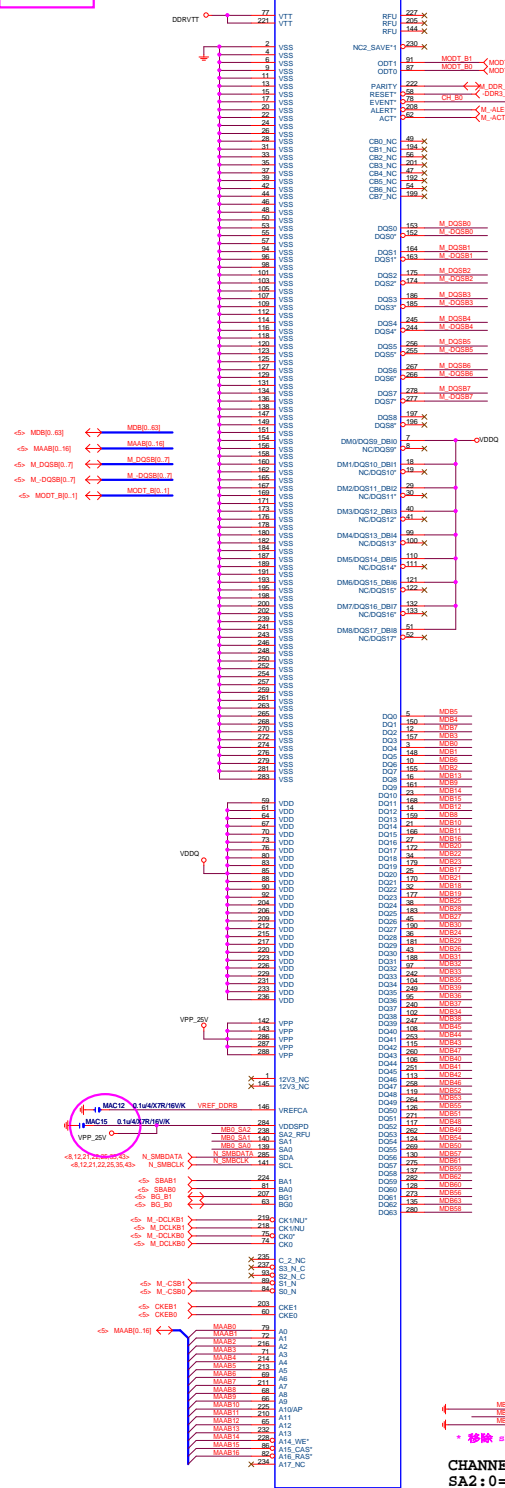
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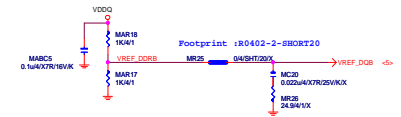
CPU POWER

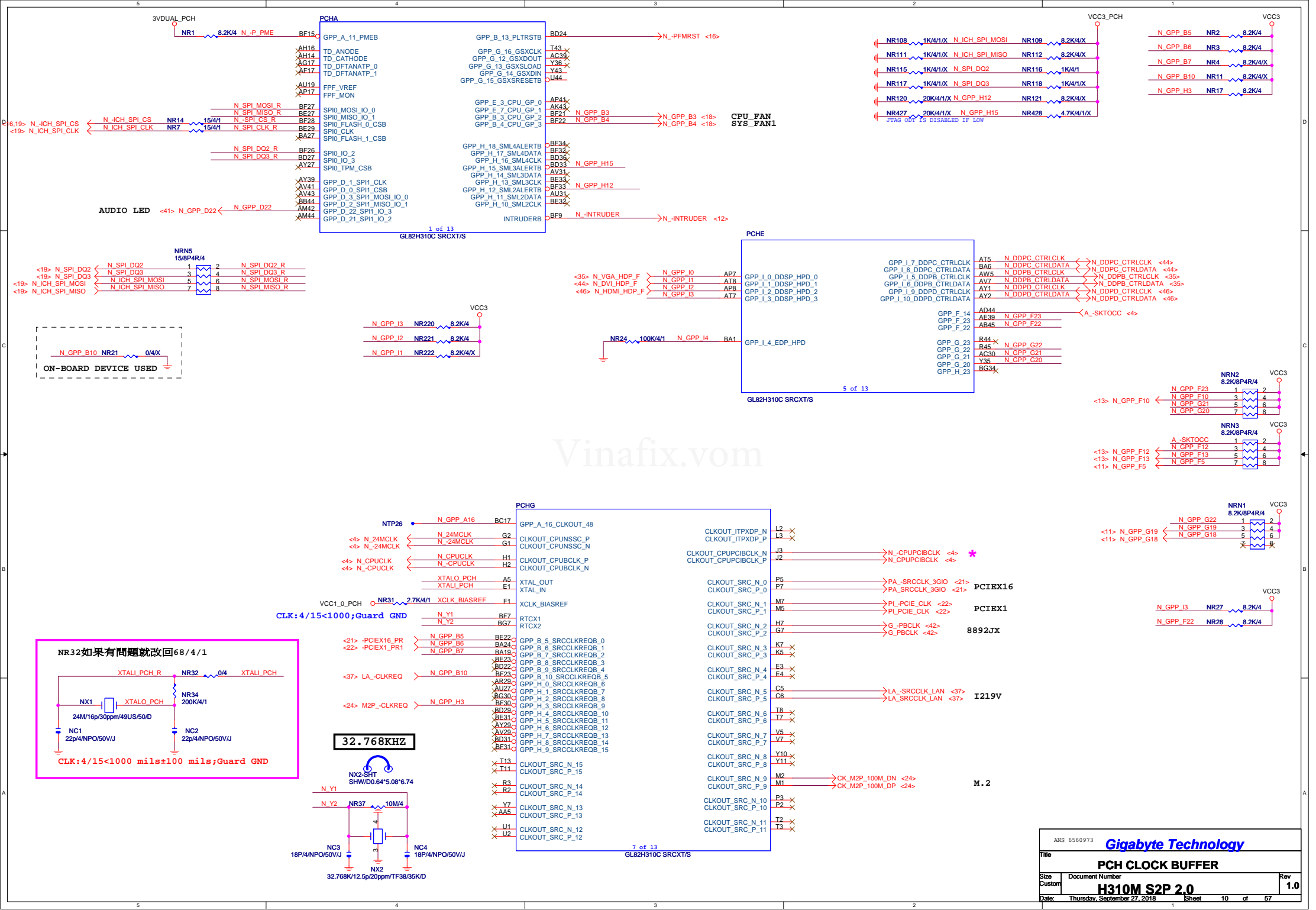


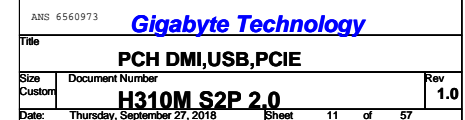


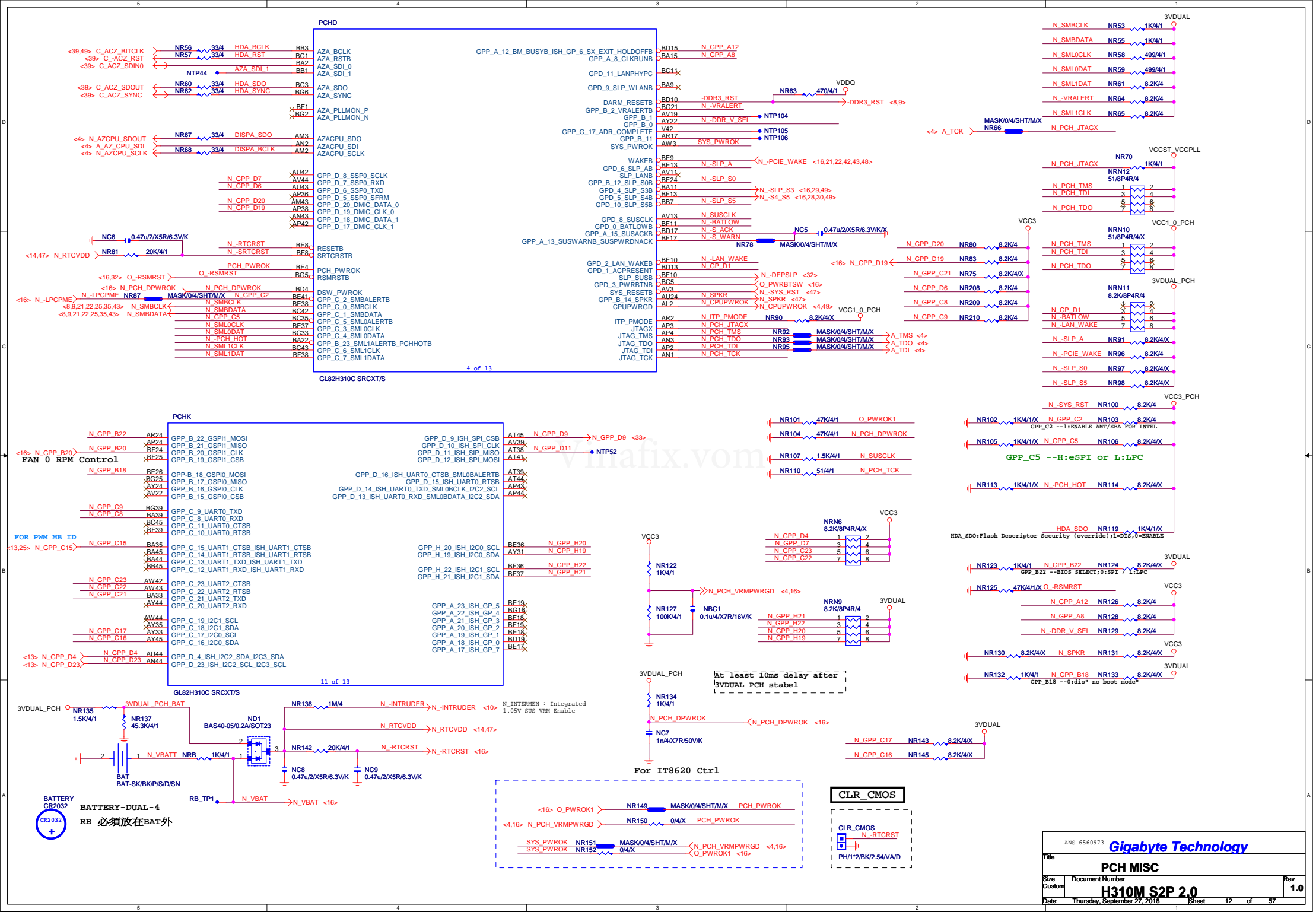


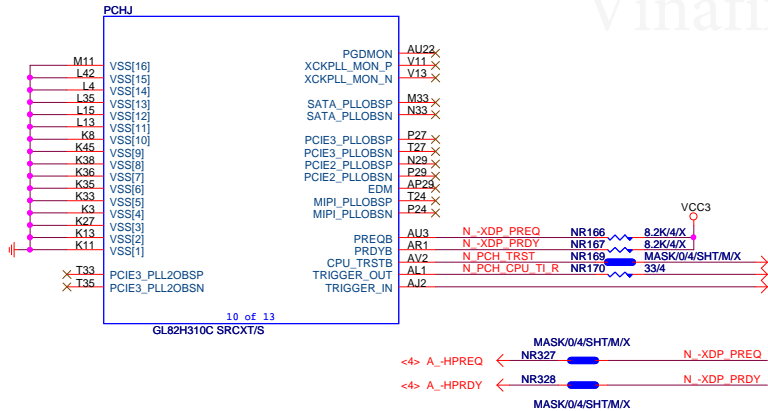
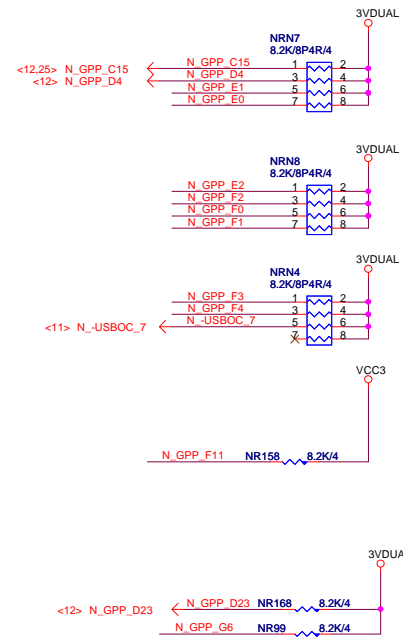
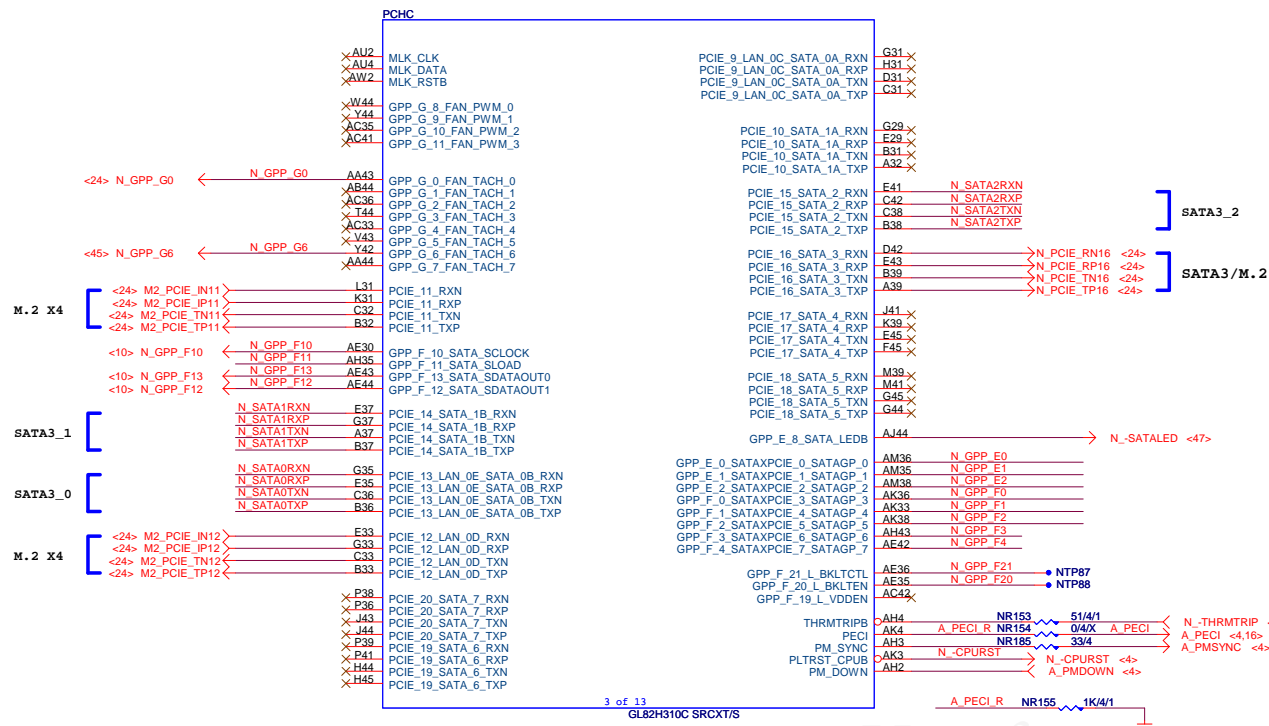
Vinafix.com



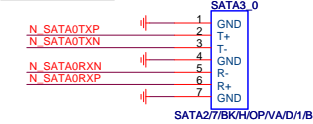




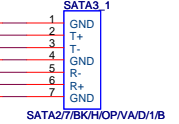




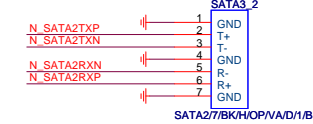
SATA3 0/1



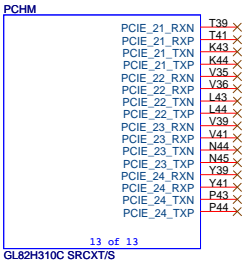
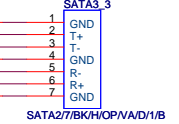
BLACK CONNECTOR

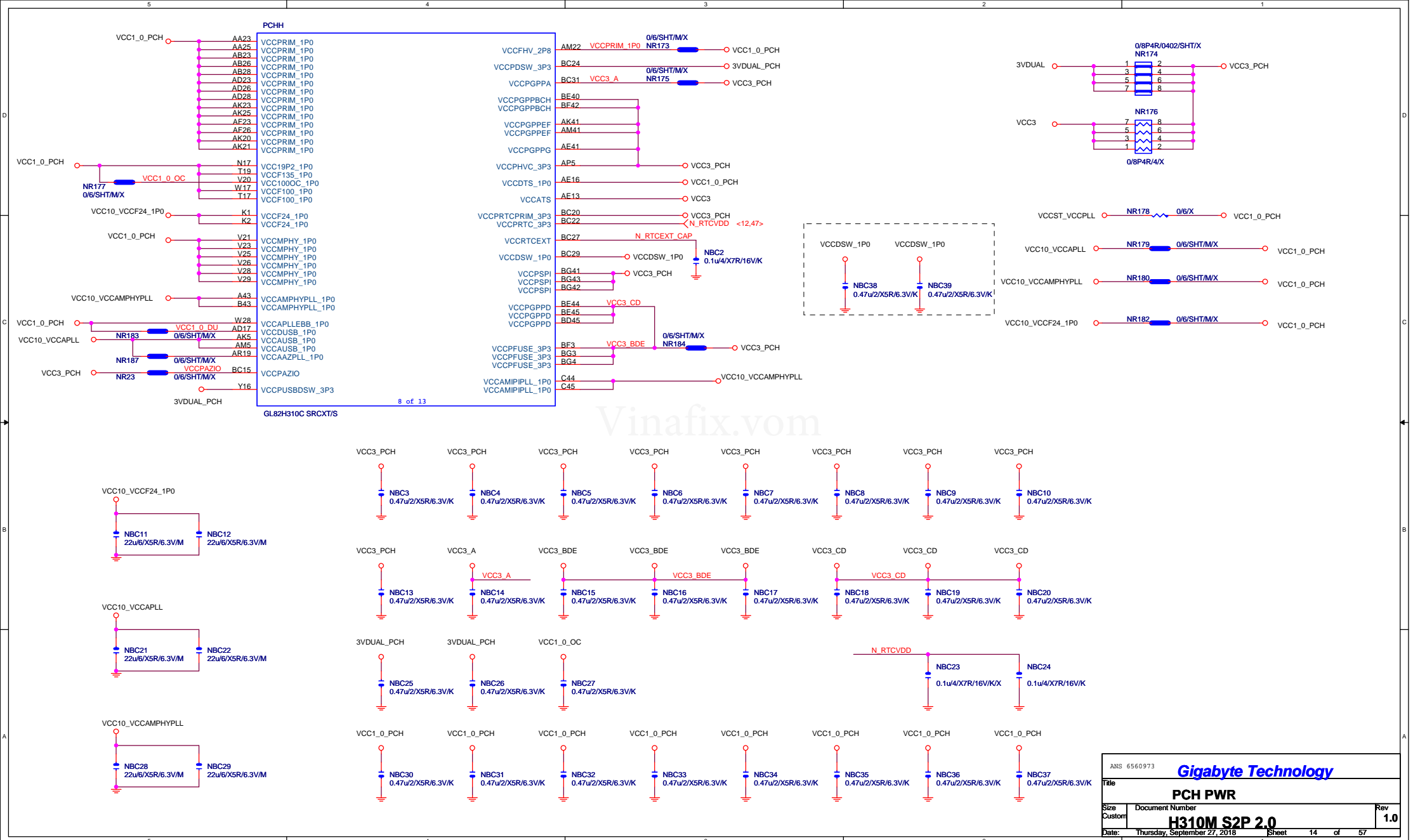


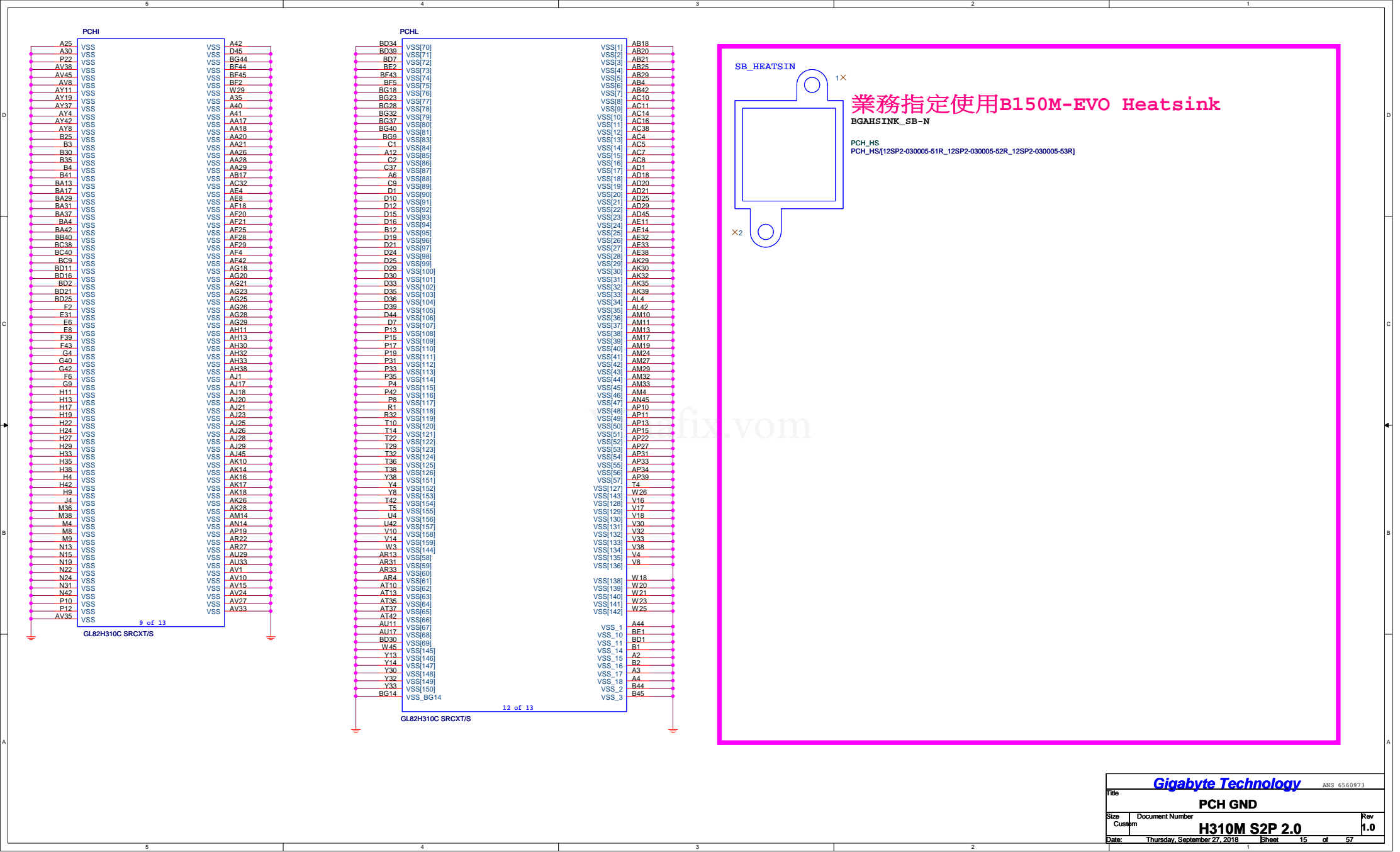
SATA3 2/3



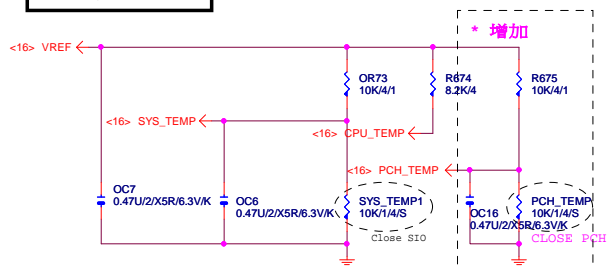
BLACK CONNECTOR



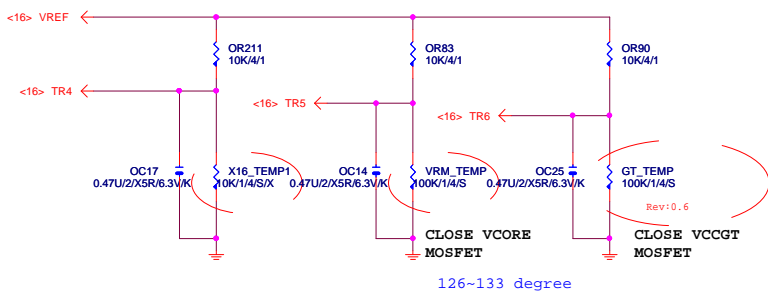




TEMP H/W MONITOR

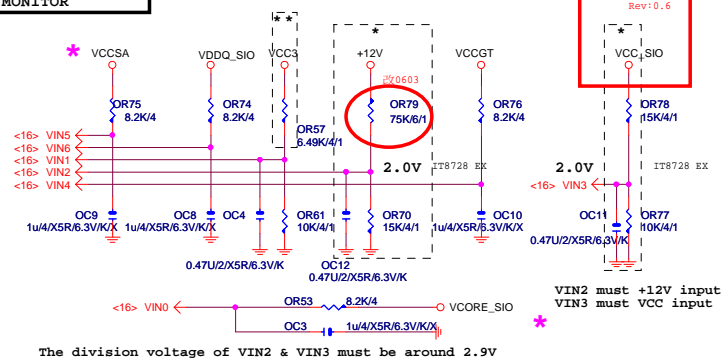


低階機種:3個FAN時使用

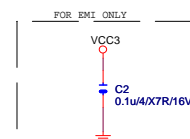
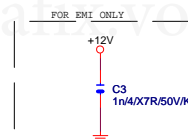


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VOLTAGE-- H/W
MONITOR

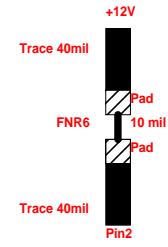
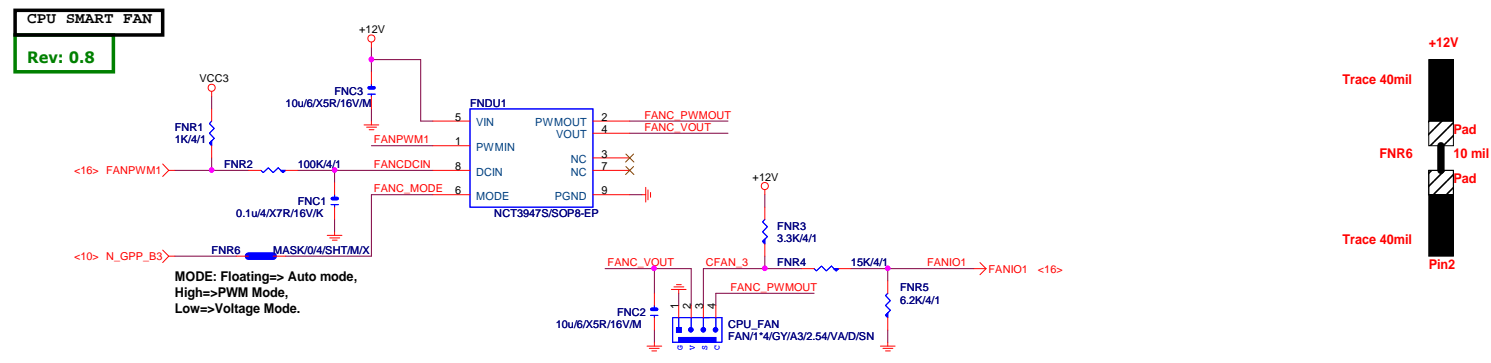


(靠近ATX CONNECTOR)

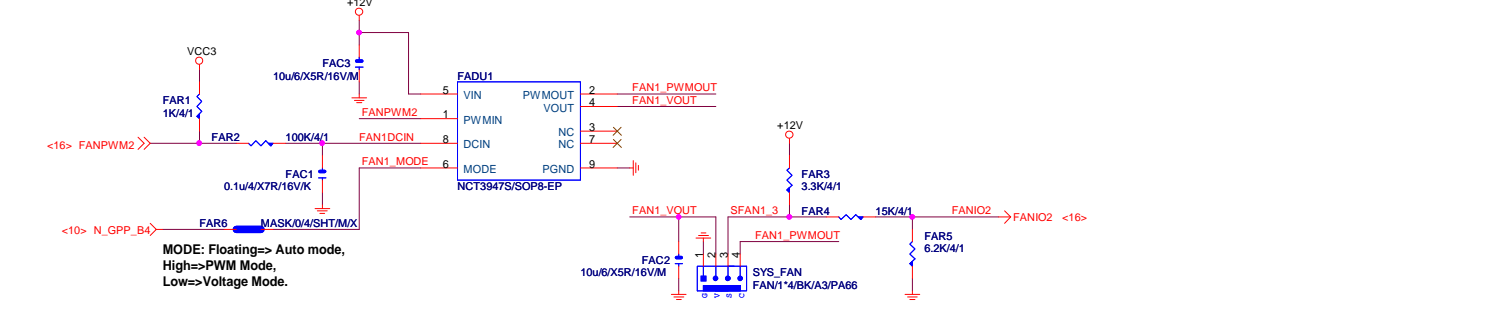


★Update 2015-04.24

CPU SMART FAN
Rev: 0.8

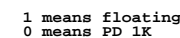


A. SYSTEM FAN1

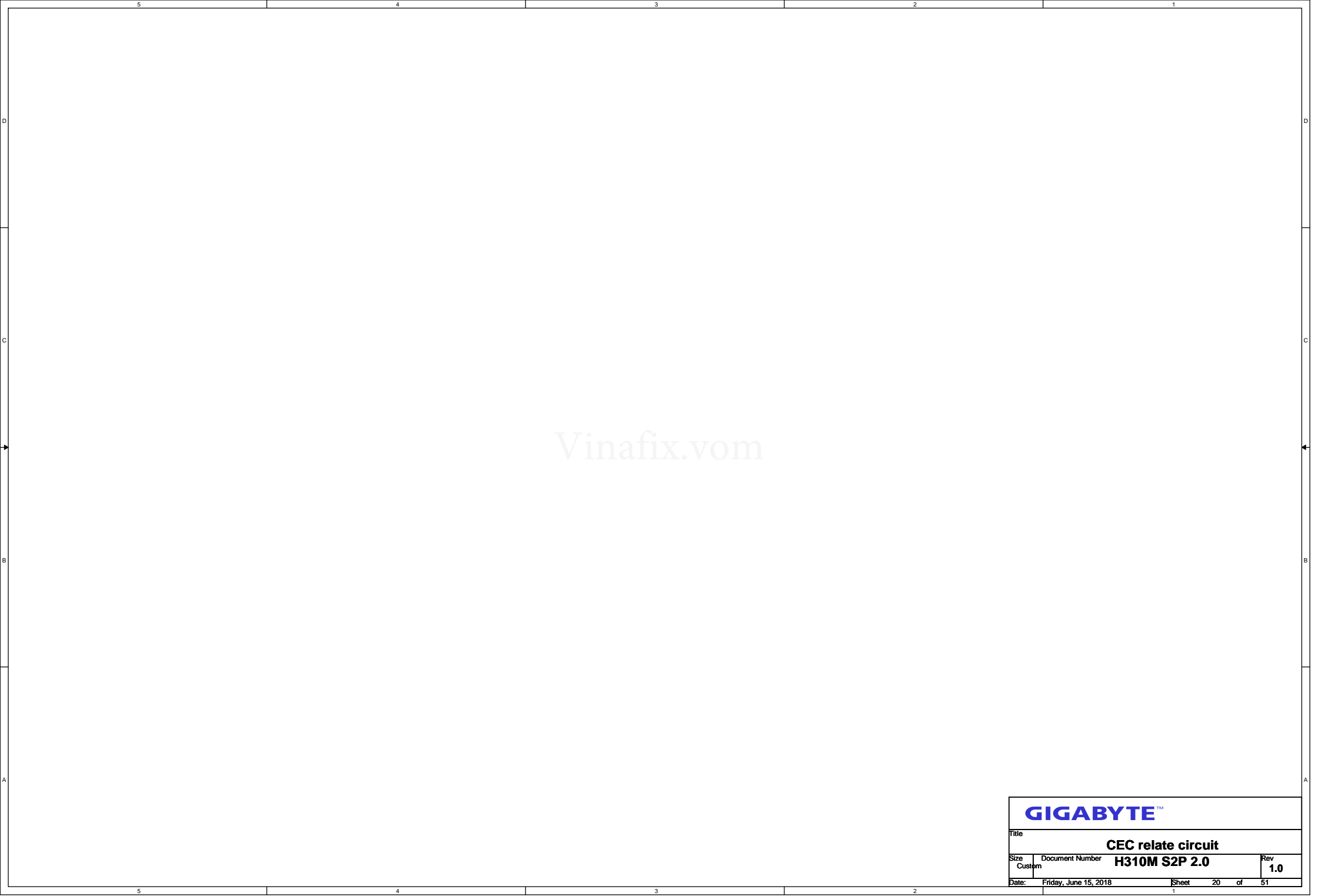


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MOSI For DMI RX Termination Voltage



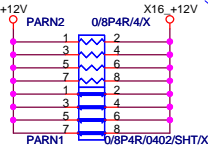
* 試產先上，PVT 移除



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GIGABYTE™			
Title			
CEC relate circuit			
Size	Document Number	Rev	
Custom	H310M S2P 2.0	1.0	
Date:	Friday, June 15, 2018	Sheet	20 of 51

+12V protect
short-wire test



PA_EXP_RXP0..15] >> PA_EXP_RXP[0..15] <<
PA_EXP_RXN0..15] >> PA_EXP_RXN[0..15] <<
PA_EXP_TXP0..15] >> PA_EXP_TXP[0..15] <<
PA_EXP_TXN0..15] >> PA_EXP_TXN[0..15] <<

PA_EXP_TXP0	PAC5	0.22u/4/X5R/6.3V/K	PA_EXP_TXP0_C
PA_EXP_TXN0	PAC4	0.22u/4/X5R/6.3V/K	PA_EXP_TXN0_C
PA_EXP_TXP1	PAC6	0.22u/4/X5R/6.3V/K	PA_EXP_TXP1_C
PA_EXP_TXN1	PAC7	0.22u/4/X5R/6.3V/K	PA_EXP_TXN1_C
PA_EXP_TXP2	PAC8	0.22u/4/X5R/6.3V/K	PA_EXP_TXP2_C
PA_EXP_TXN2	PAC9	0.22u/4/X5R/6.3V/K	PA_EXP_TXN2_C
PA_EXP_TXP3	PAC10	0.22u/4/X5R/6.3V/K	PA_EXP_TXP3_C
PA_EXP_TXN3	PAC11	0.22u/4/X5R/6.3V/K	PA_EXP_TXN3_C
PA_EXP_TXP4	PAC12	0.22u/4/X5R/6.3V/K	PA_EXP_TXP4_C
PA_EXP_TXN4	PAC13	0.22u/4/X5R/6.3V/K	PA_EXP_TXN4_C
PA_EXP_TXP5	PAC14	0.22u/4/X5R/6.3V/K	PA_EXP_TXP5_C
PA_EXP_TXN5	PAC15	0.22u/4/X5R/6.3V/K	PA_EXP_TXN5_C
PA_EXP_TXP6	PAC16	0.22u/4/X5R/6.3V/K	PA_EXP_TXP6_C
PA_EXP_TXN6	PAC17	0.22u/4/X5R/6.3V/K	PA_EXP_TXN6_C
PA_EXP_TXP7	PAC18	0.22u/4/X5R/6.3V/K	PA_EXP_TXP7_C
PA_EXP_TXN7	PAC19	0.22u/4/X5R/6.3V/K	PA_EXP_TXN7_C
PA_EXP_TXP8	PAC20	0.22u/4/X5R/6.3V/K	PA_EXP_TXP8_C
PA_EXP_TXN8	PAC21	0.22u/4/X5R/6.3V/K	PA_EXP_TXN8_C
PA_EXP_TXP9	PAC22	0.22u/4/X5R/6.3V/K	PA_EXP_TXP9_C
PA_EXP_TXN9	PAC23	0.22u/4/X5R/6.3V/K	PA_EXP_TXN9_C
PA_EXP_TXP10	PAC24	0.22u/4/X5R/6.3V/K	PA_EXP_TXP10_C
PA_EXP_TXN10	PAC25	0.22u/4/X5R/6.3V/K	PA_EXP_TXN10_C
PA_EXP_TXP11	PAC26	0.22u/4/X5R/6.3V/K	PA_EXP_TXP11_C
PA_EXP_TXN11	PAC27	0.22u/4/X5R/6.3V/K	PA_EXP_TXN11_C
PA_EXP_TXP12	PAC28	0.22u/4/X5R/6.3V/K	PA_EXP_TXP12_C
PA_EXP_TXN12	PAC29	0.22u/4/X5R/6.3V/K	PA_EXP_TXN12_C
PA_EXP_TXP13	PAC30	0.22u/4/X5R/6.3V/K	PA_EXP_TXP13_C
PA_EXP_TXN13	PAC31	0.22u/4/X5R/6.3V/K	PA_EXP_TXN13_C
PA_EXP_TXP14	PAC32	0.22u/4/X5R/6.3V/K	PA_EXP_TXP14_C
PA_EXP_TXN14	PAC33	0.22u/4/X5R/6.3V/K	PA_EXP_TXN14_C
PA_EXP_TXP15	PAC34	0.22u/4/X5R/6.3V/K	PA_EXP_TXP15_C
PA_EXP_TXN15	PAC35	0.22u/4/X5R/6.3V/K	PA_EXP_TXN15_C

<8,9,12,22,25,35,43> N_SMBCLK
<8,9,12,22,25,35,43> N_SMBDATA
<12,16,22,42,43,48> N_-PCIE_WAKE

<10> -PCIE16_PR

PA_EXP_TXP0_C

PA_EXP_TXN0_C

PA_EXP_TXP1_C

PA_EXP_TXN1_C

PA_EXP_TXP2_C

PA_EXP_TXN2_C

PA_EXP_TXP3_C

PA_EXP_TXN3_C

PA_EXP_TXP4_C

PA_EXP_TXN4_C

PA_EXP_TXP5_C

PA_EXP_TXN5_C

PA_EXP_TXP6_C

PA_EXP_TXN6_C

PA_EXP_TXP7_C

PA_EXP_TXN7_C

PA_EXP_TXP8_C

PA_EXP_TXN8_C

PA_EXP_TXP9_C

PA_EXP_TXN9_C

PA_EXP_TXP10_C

PA_EXP_TXN10_C

PA_EXP_TXP11_C

PA_EXP_TXN11_C

PA_EXP_TXP12_C

PA_EXP_TXN12_C

PA_EXP_TXP13_C

PA_EXP_TXN13_C

PA_EXP_TXP14_C

PA_EXP_TXN14_C

PA_EXP_TXP15_C

PA_EXP_TXN15_C

PCIEX16:16/5/5/5/16

PCI-E REV:1.1--> 2.5GHZ

PCE-E X1(單向) BANDWITH=2.5GHz*(8b/10b)=2Gb/s=250MB/s

PCE-E X1(雙向) BANDWITH=2.5GHz*(8b/10b)X2=4Gb/s=500MB/s

PCE-E X16(單向) BANDWITH=2.5GHz*(8b/10b)X16=32Gb/s=4GB/s

PCE-E X16(雙向) BANDWITH=2.5GHz*(8b/10b)X16X2=64Gb/s=8GB/s

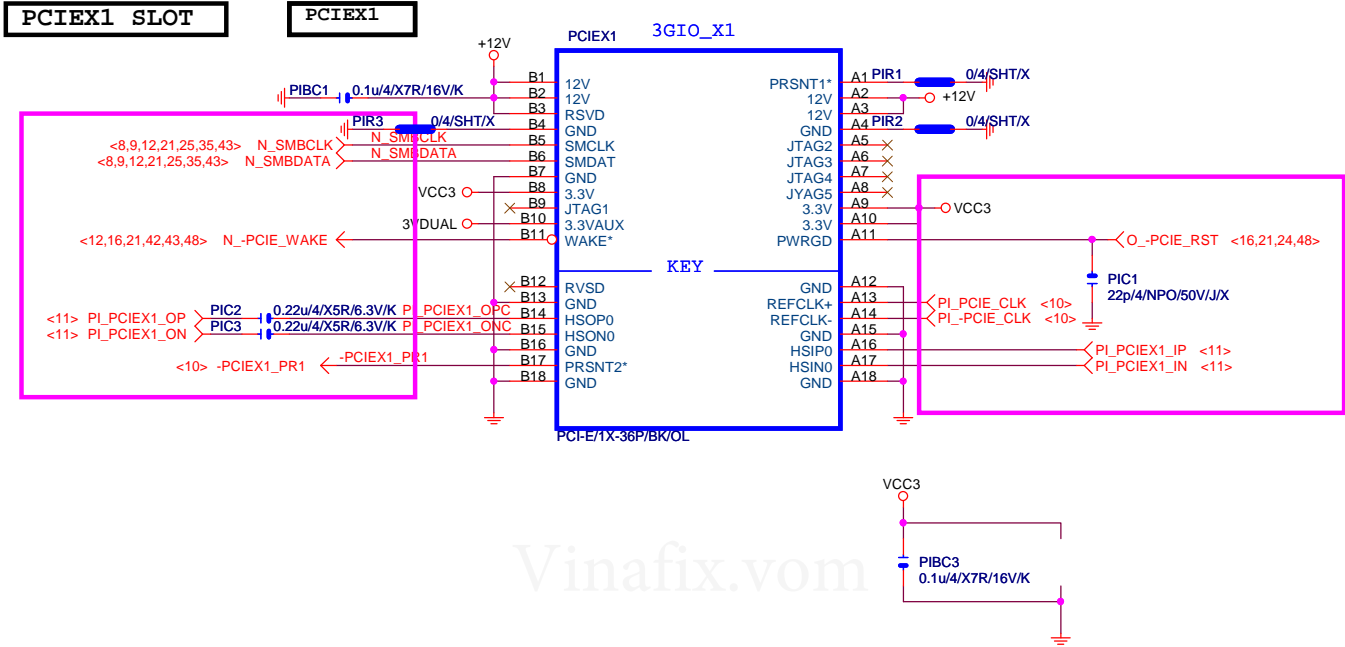
PCI-E REV:2.0--> 5GHZ

PCIESLOT-164P

3GIO_*16

PCI-E/16X-164P/GY/LONG DOUBLE/HK*2

灰色SLOT



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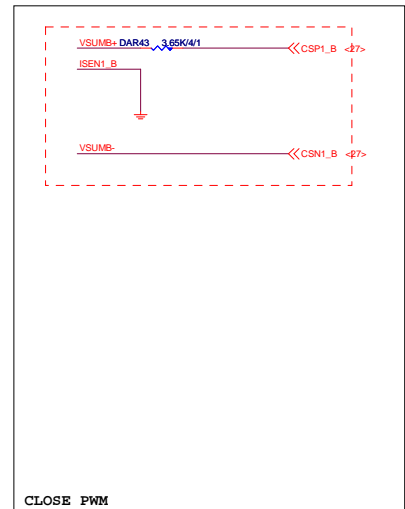
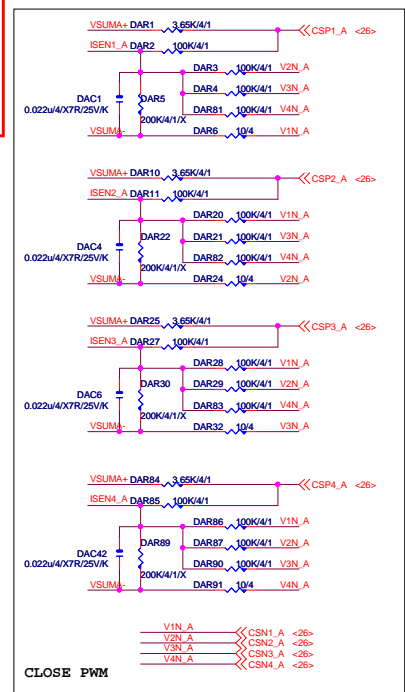
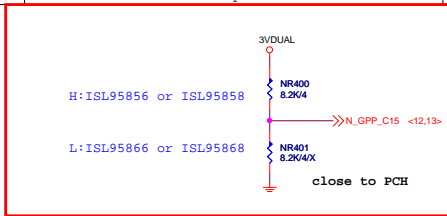
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Gigabyte Technology			
Title			
PCIE_X4			
Size	Document Number		Rev
Custom	H310M S2P 2.0		1.0
Date:	Friday, June 15, 2018		Sheet 22 of 51

	5	4	3	2	1
D					
C					
B					
A					

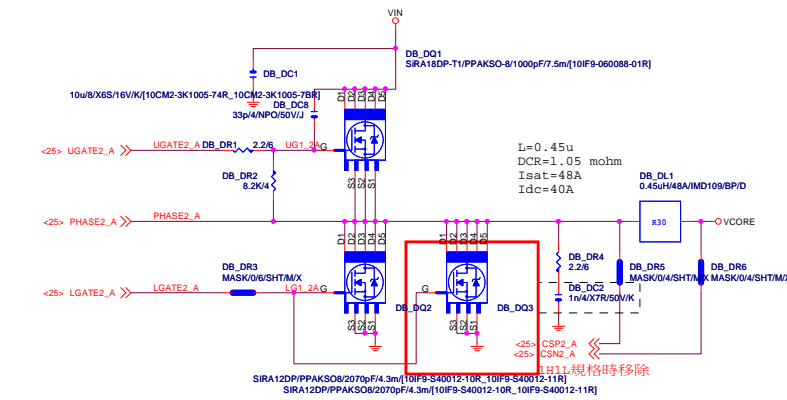
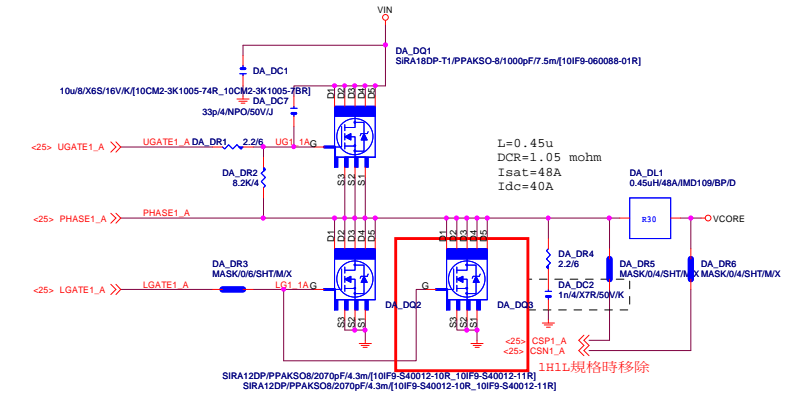
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Gigabyte Technology				
Title				
SATA				
Size	Document Number			Rev
Custom	H310M S2P 2.0			1.0
Date:	Friday, June 15, 2018		Sheet	23 of 51

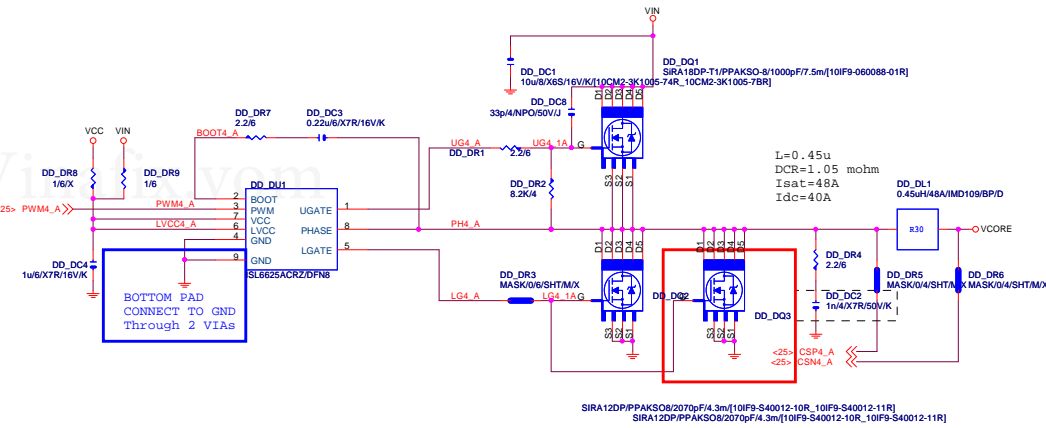
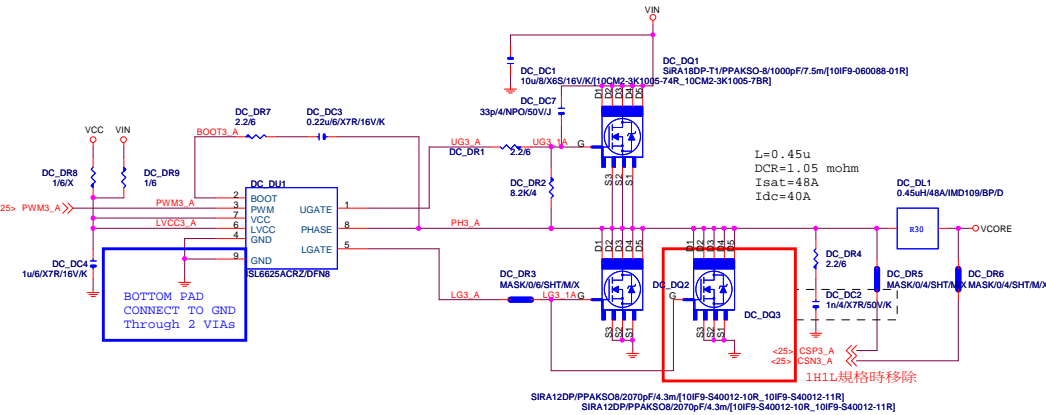
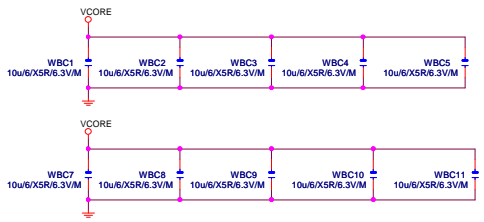
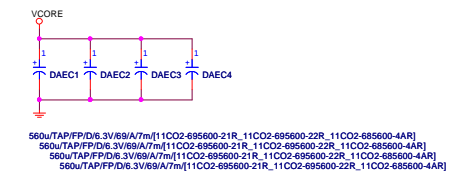


VCORE	ISL95856	ISL95866	VCCGT	ISL95856	ISL95866
DAR137	X	V	DAR140	X	V
DAR138	V	X	DAR141	V	X
DAR139	X	V	DAR142	X	V
DAC15	V	X	DAC27	V	X
DAR79	V	X	DAR80	V	X
DAR33	V	X	DAR51	V	X

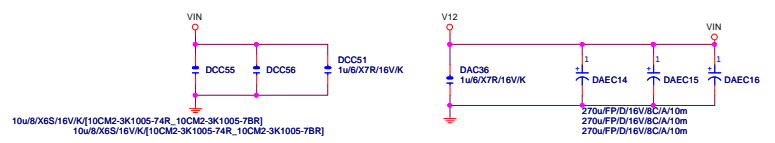
VCORE



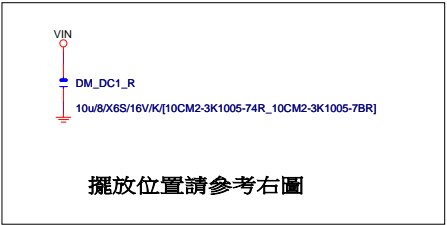
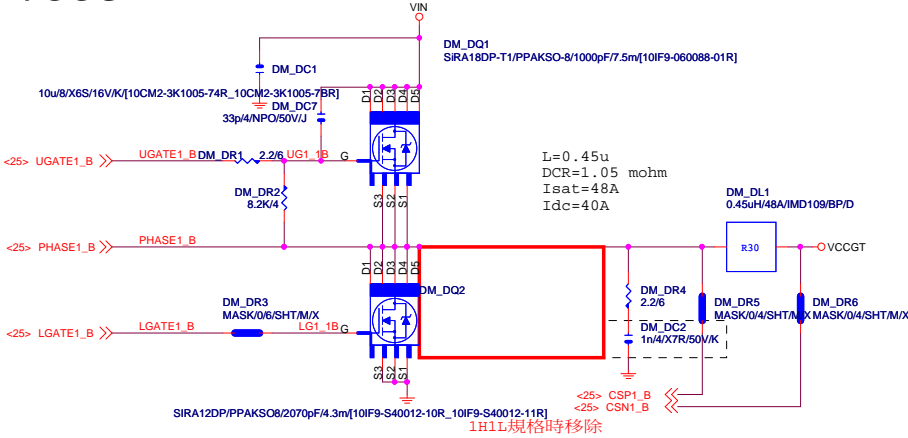
VCORE CAP 560u*4PCS
22u*10PCS



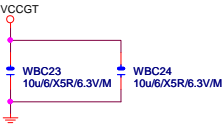
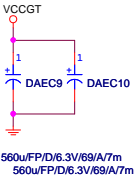
VIN CAP 270u*3PCS



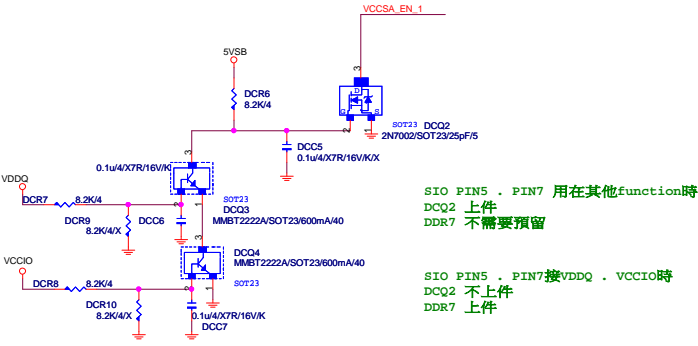
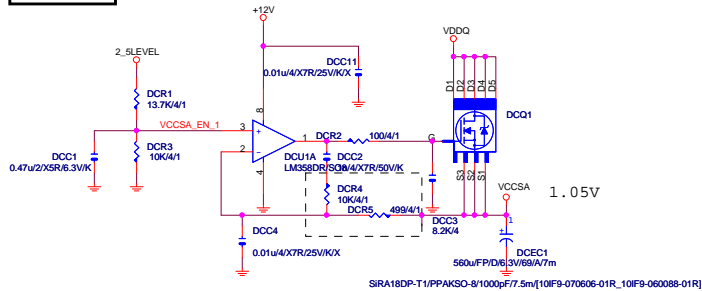
VCCGT



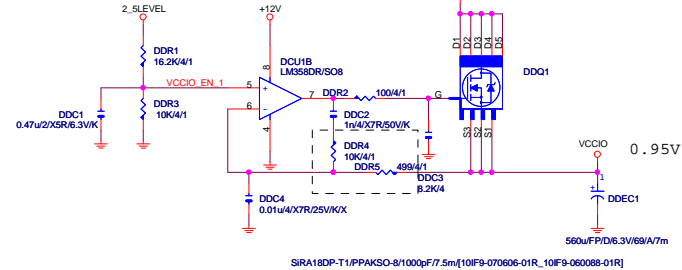
VCCGT CAP 560u*2PCS
22u*2PCS



VCCSA

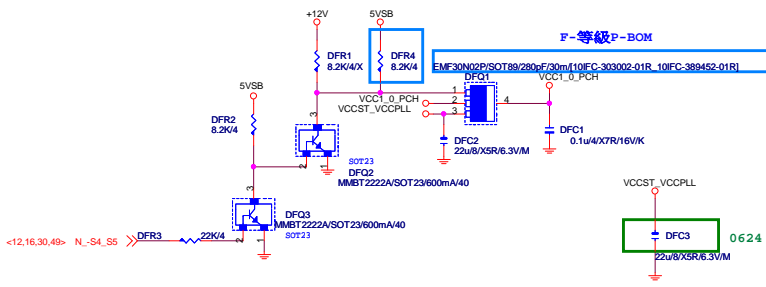


VCCIO



VCCIO_EN 1 DDR8 0.01uF/4X7R/25V/K/X VCCIO_EN <16>
Connect to IT8686

VCCST_VCCPLL



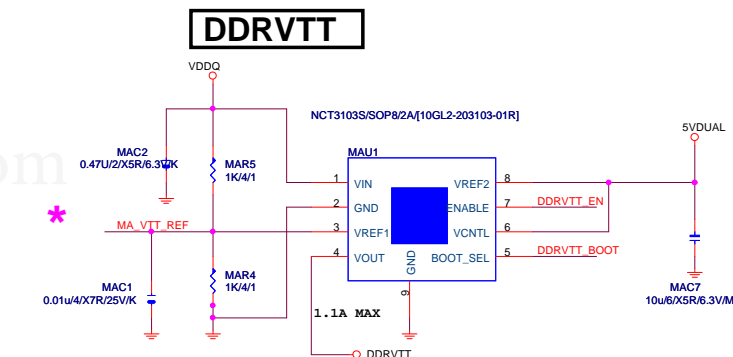


CLOSE TO DDR POWER PLANE

For power sequence require

VPP_25V使用8120時上件

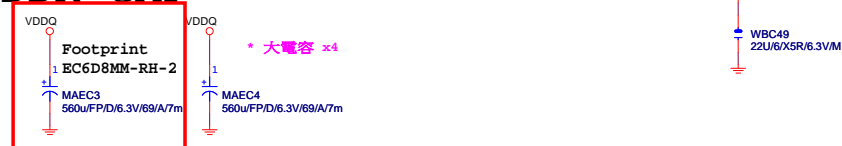
MAU1上RT9045時上件(不可MASK)



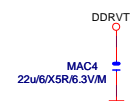
DDRVTT

<4> DDR_VTT_CTL MAR110 MASK/0/4/SHT/M/10/X DDRVTT_EN
 <12.16.49> N_SLP_S3 MAR111 MASK/0/4/SHT/M/10/X DDRVTT_BOOT

DDR CAP 560u*4PCS 22u*2PCS



DDRVTT CAP

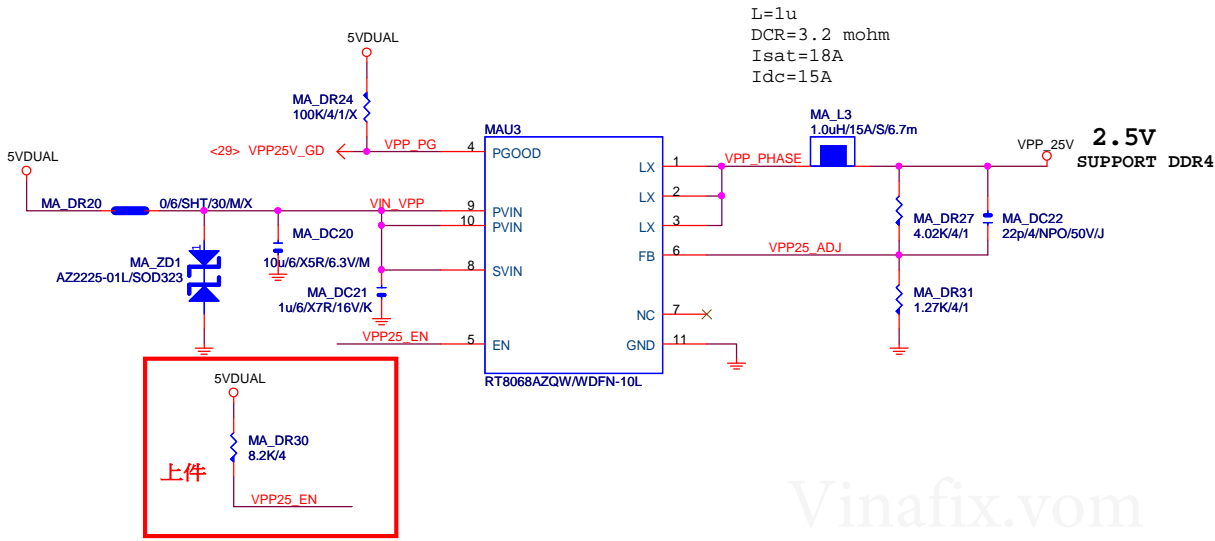


Vinafix.com

REV:0.1

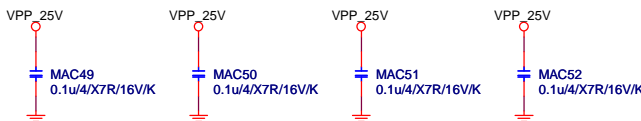
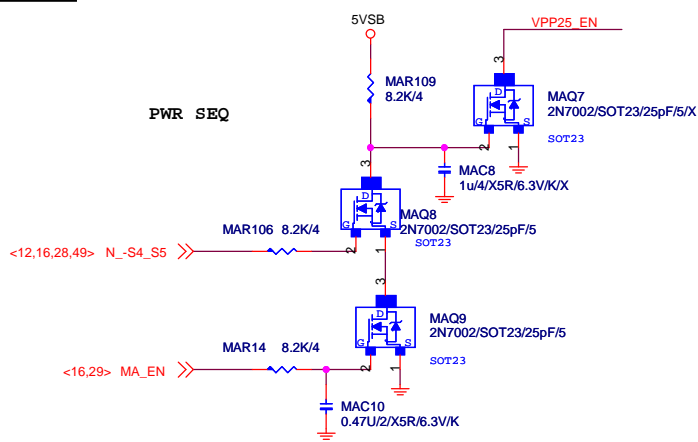
VPP_25V

CHOKE與CAP料號可變



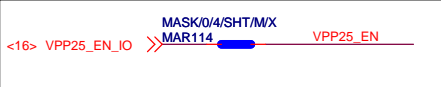
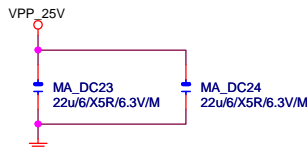
PWR_SEQ

* 刪 MA_DR32



VPP CAP 22u*1PCS

* 大電容 x0

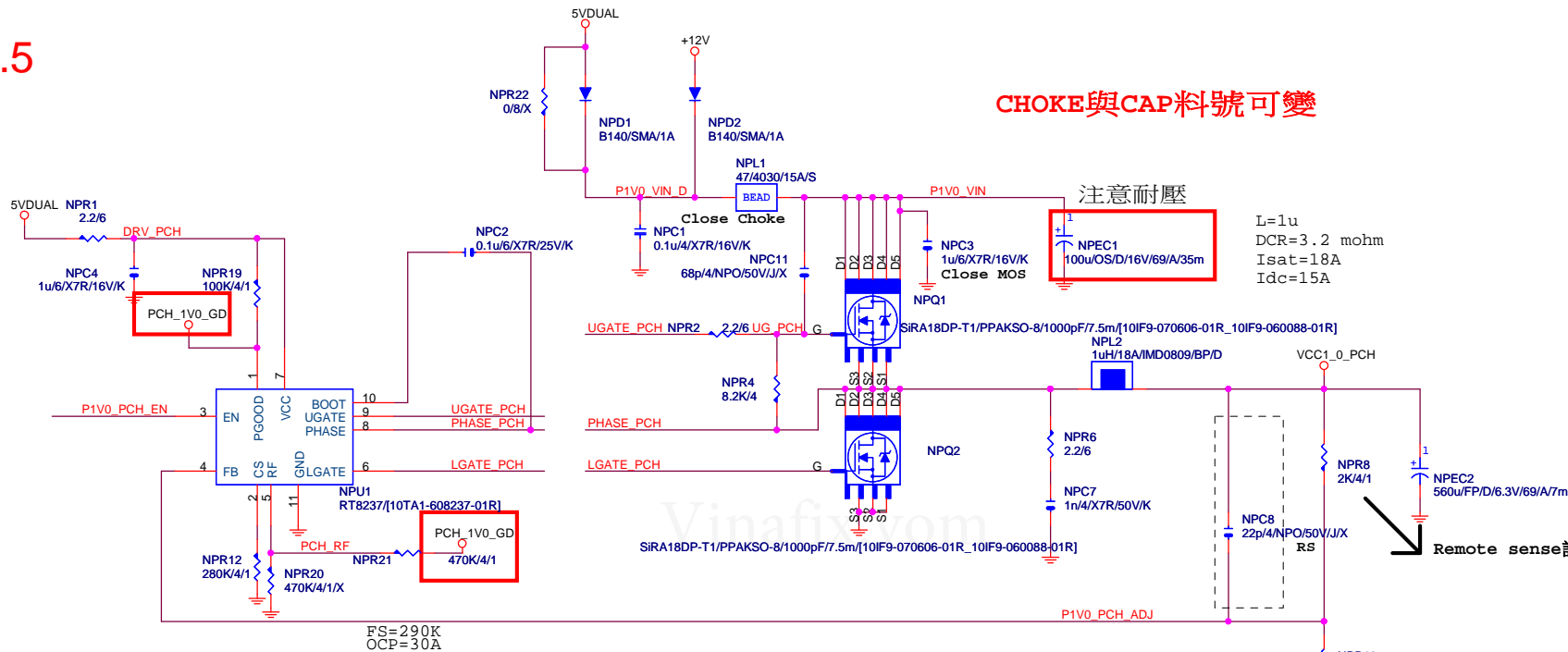


GIGABYTE™

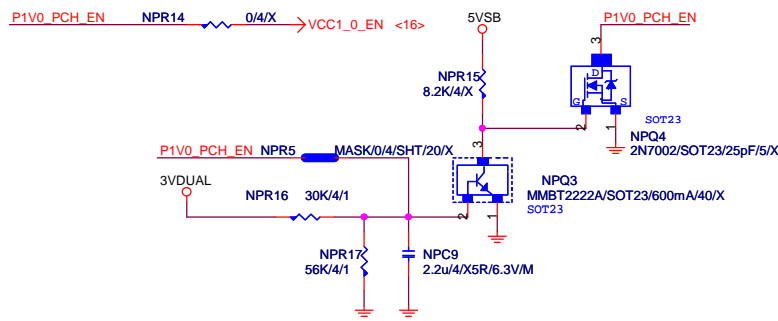
Title RT8068A_VPP25 POWER		
Size Custom	Document Number H310M S2P 2.0	Rev 1.0
Date: Friday, June 15, 2018 Sheet 30 of 51		

REV:0.5

CHOKE與CAP料號可變



PWR SEQ



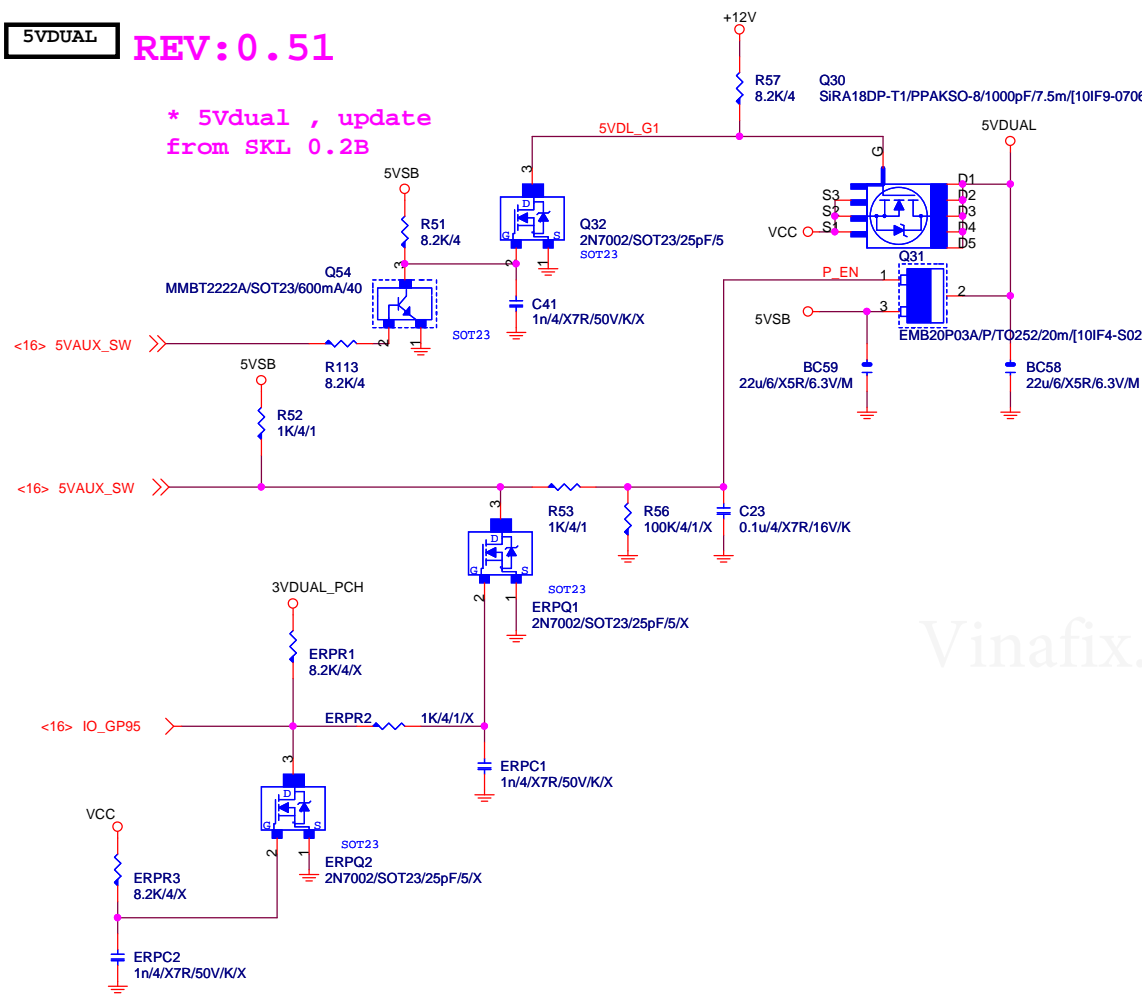
請放置CHOKE一出來的地方

GIGABYTE™			
Title			
RT8237_PCH POWER			
Size	Document Number	Rev	
Custom	H310M S2P 2.0	1.0	
Date:	Tuesday, June 19, 2018	Sheet	31 of 51

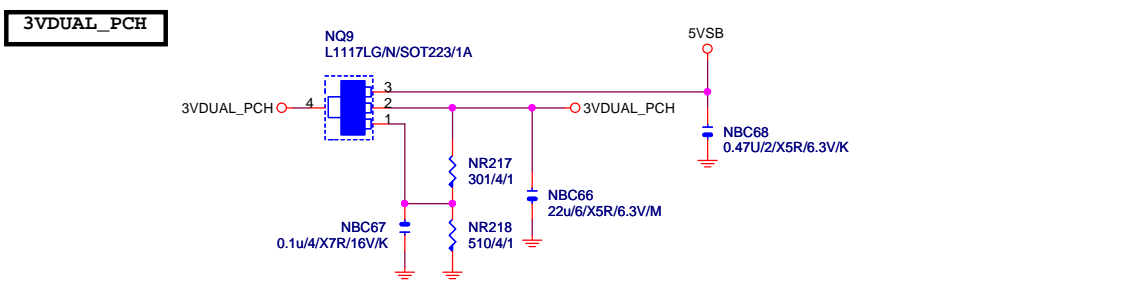
5VDUAL

REV:0.51

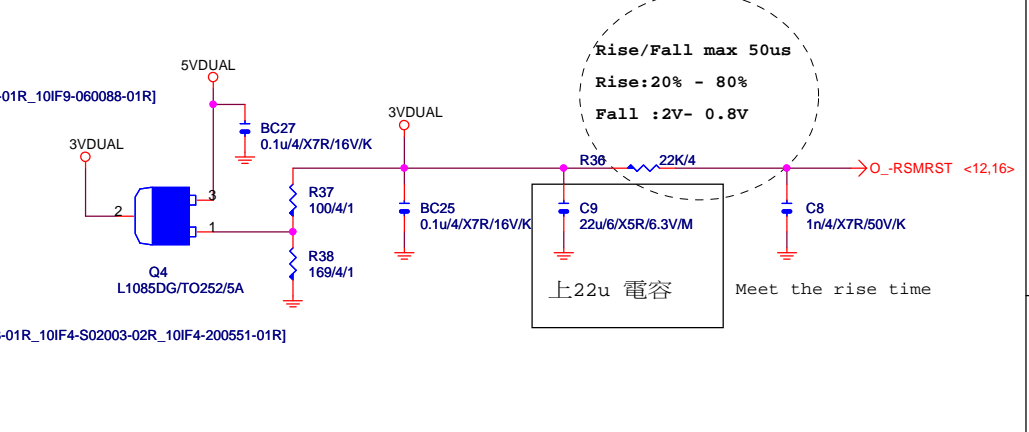
* 5Vdual , update
from SKL 0.2B



3VDUAL_PCH



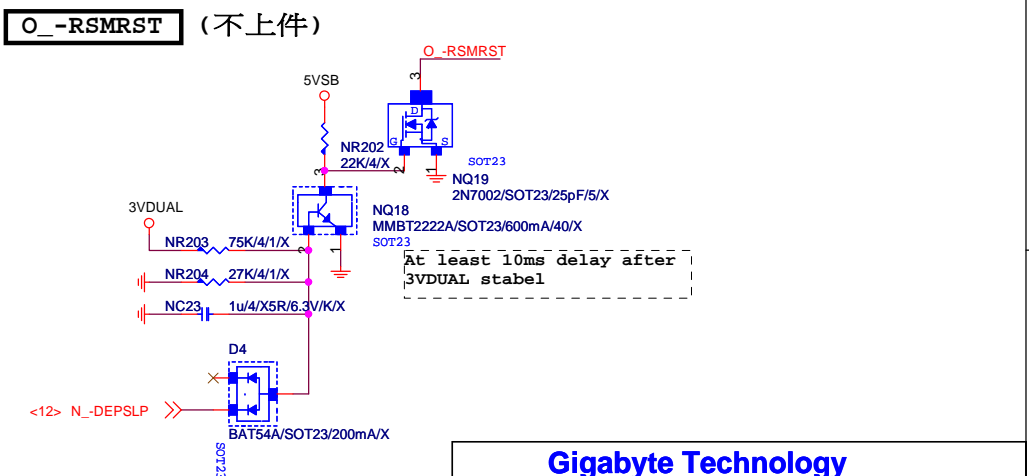
3VDUAL



Vinafix.com

O_-RSMRST

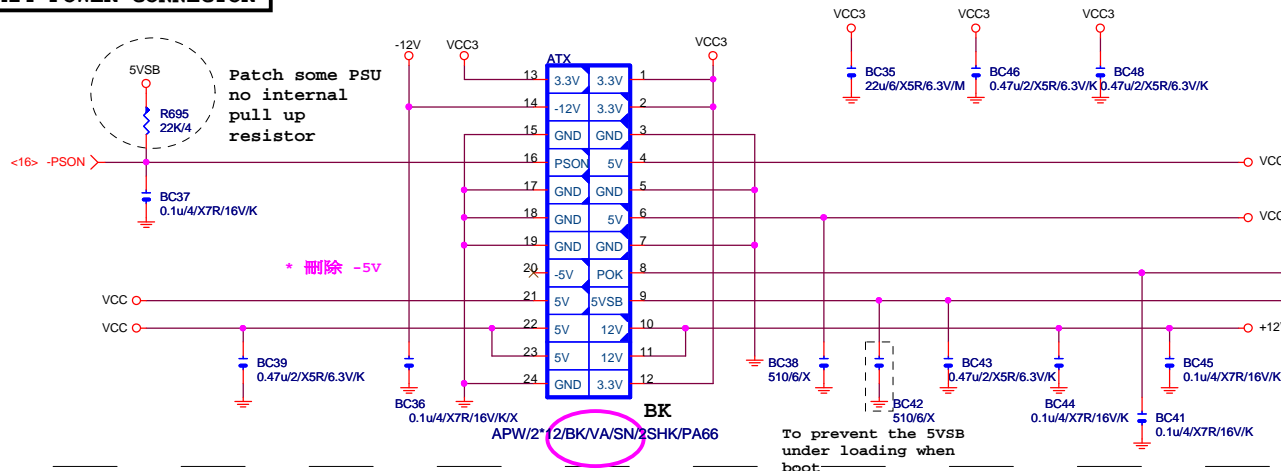
(不上件)



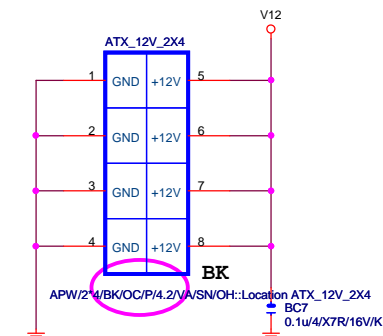
Gigabyte Technology

Title			
DISCRETE POWER			
Size	Document Number	H310M S2P 2.0	
Custom		Rev 1.0	
Date:	Tuesday, June 19, 2018	Sheet	32 of 51

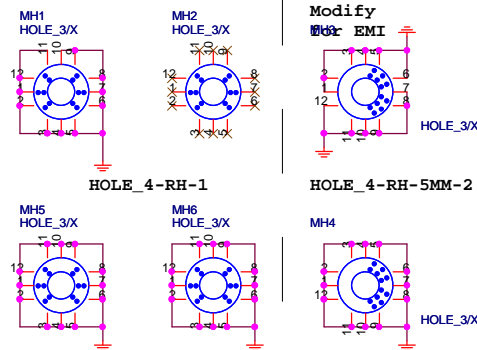
ATXX24 POWER CONNECTOR



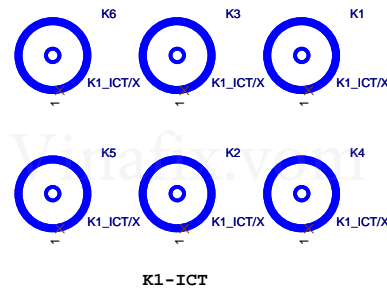
ATXX4 POWER CONNECTOR



螺絲孔



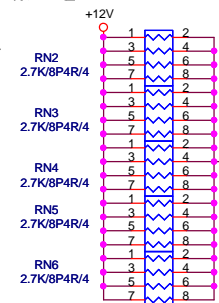
固定孔/光學點



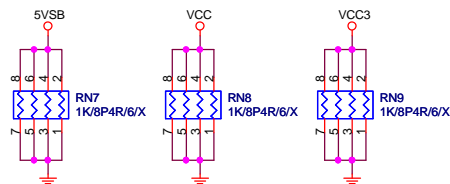
+12V DUMMY LOAD

【技術通報R&D技術通報153】

To fix 12V light load abnormal issue



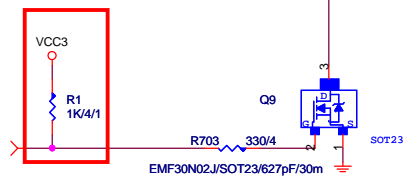
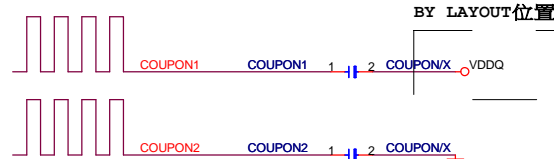
DUMMY LOAD



-PROHOT

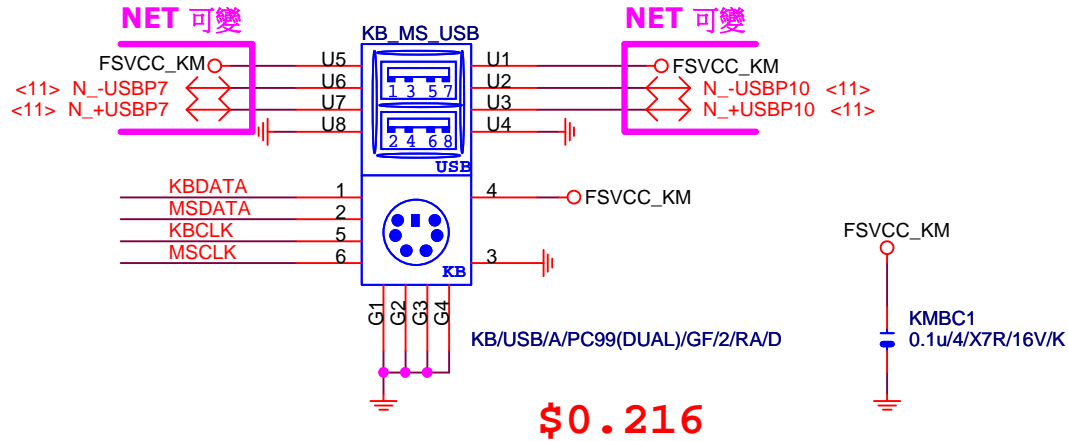
<4,16> A_PROHOT <-> A_PROCHOTR2 MASK/0/4/SHT7/M/X <-> VR_HOT <25>

COUPON

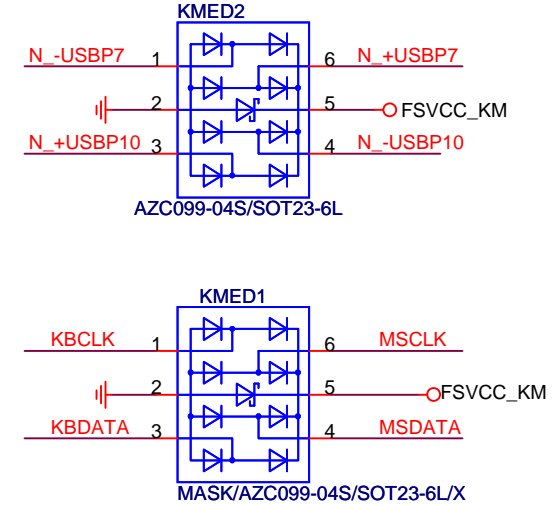


KB_MS_USB

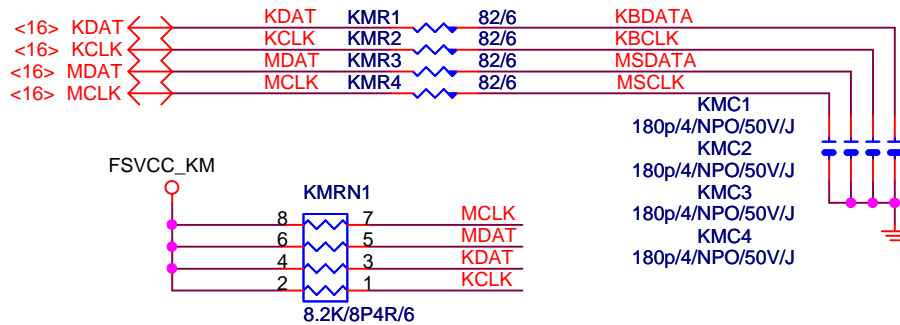
Rev: 0.2



ESD

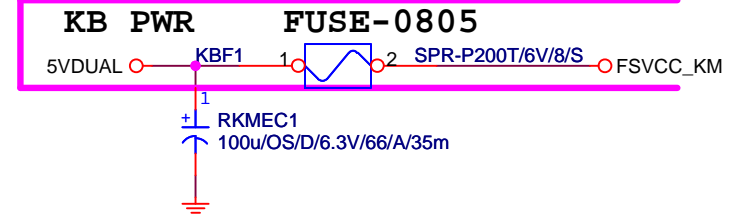


KB_MS_USB DAMPING/PU

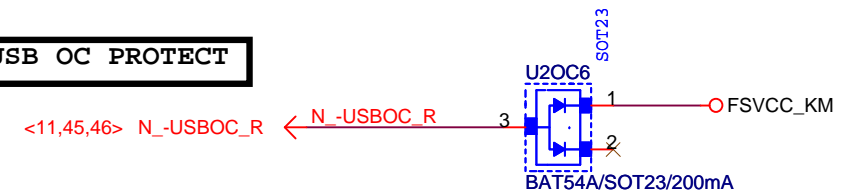


KB_MS_USB PWR

NET 可變, 與其他USB SHARE



USB OC PROTECT



Gigabyte Technology

Title

KB_MS

Size A

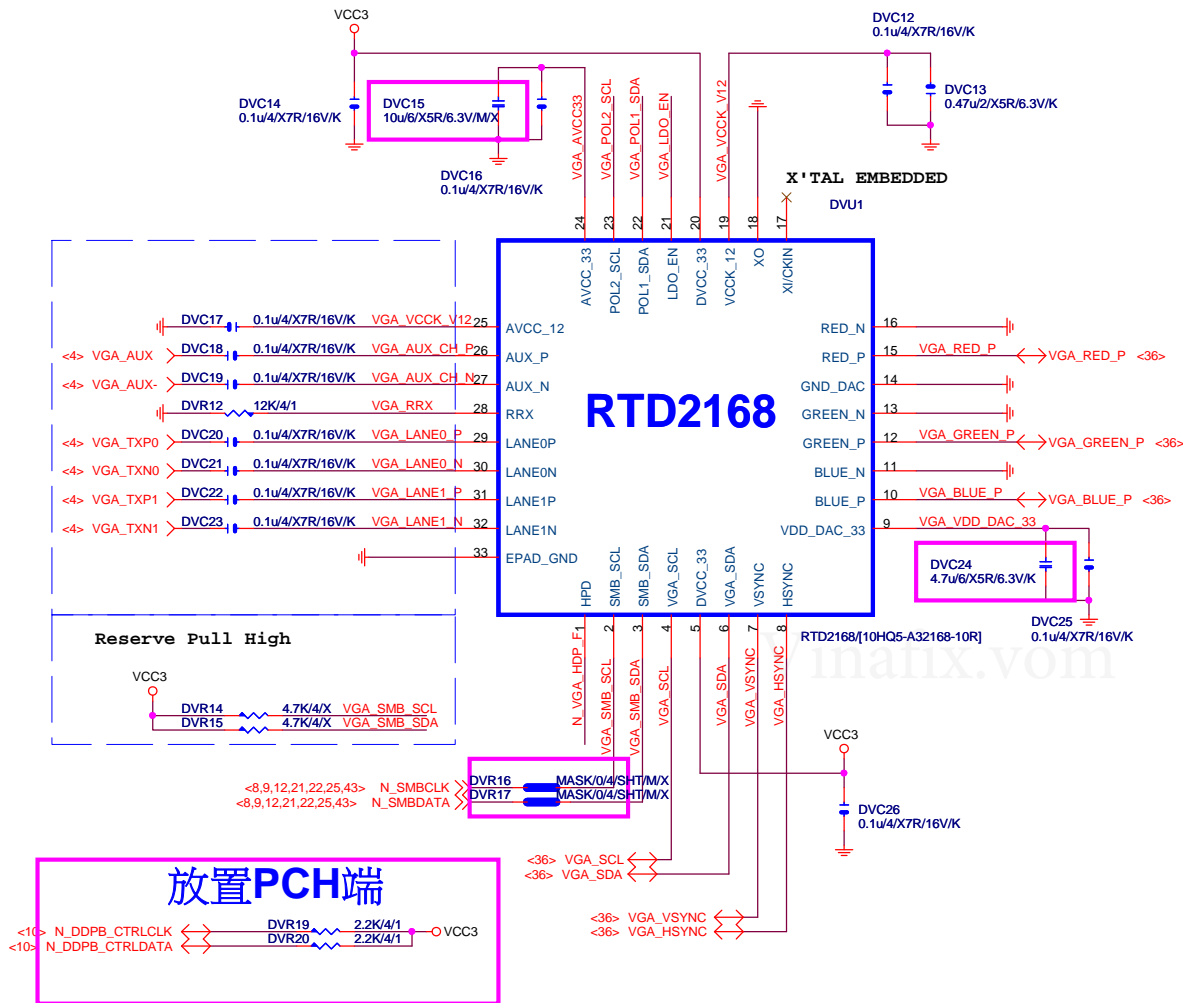
Document Number

H310M S2P 2.0

Rev 1.0

Date: Friday, June 15, 2018

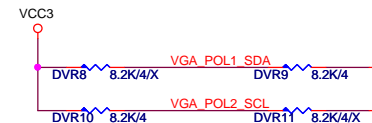
Sheet 34 of 51



POWER

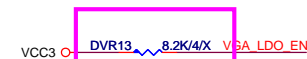


Power on latch



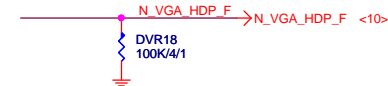
		POL1_SDA(PIN22)	
		0	1
POL2_SCL (PIN23)	0	X	EP MODE
	1	ROM ONLY MODE	EEPROM MODE

Embedded LDO



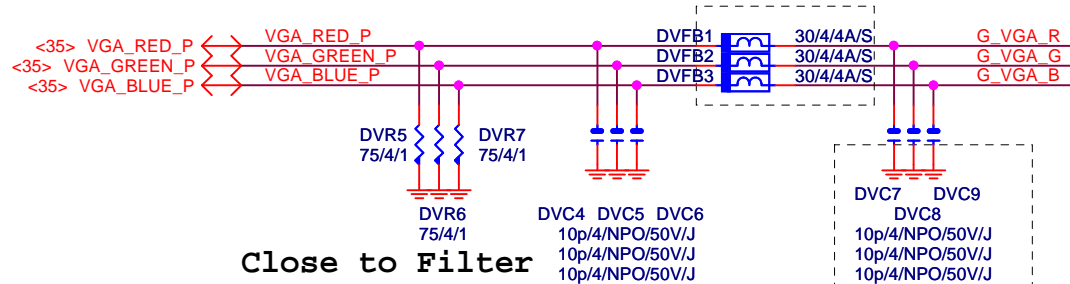
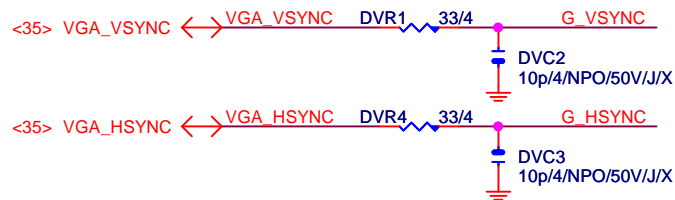
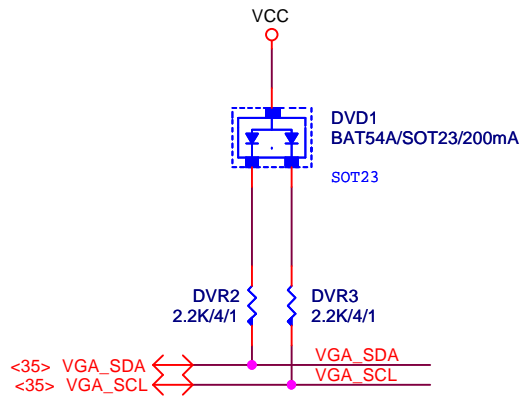
		LDO_EN(PIN21)	
		0	1
VCCAUX_V12 from External 1.2V			VCCAUX_V12 from Embedded LDO

DP HPD



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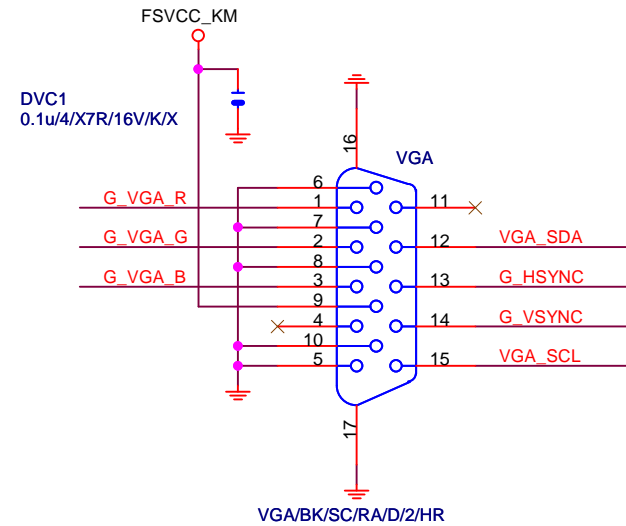
VGA SIGNAL R1.03



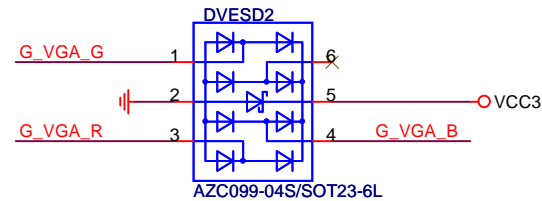
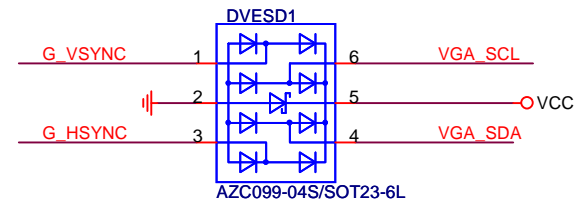
Close to Filter

FOR EMI

VGA CONN.



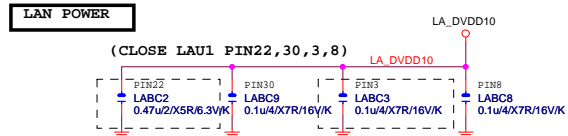
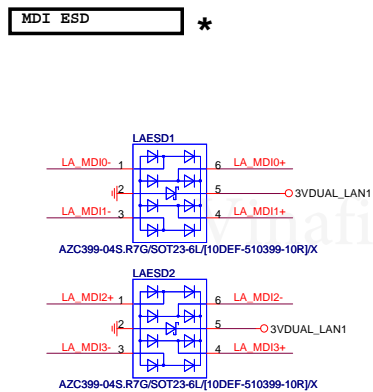
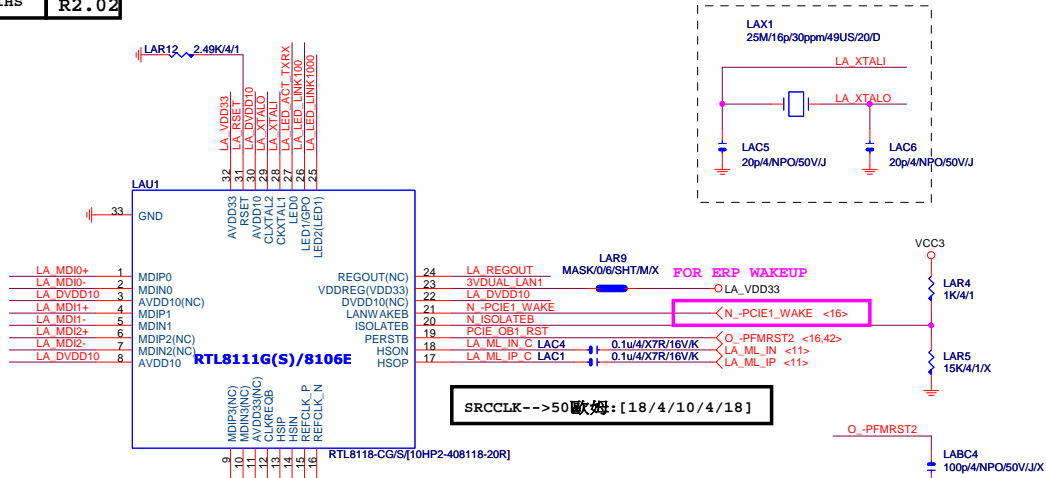
VGA ESD



Gigabyte Technology
NXP-PTN3356

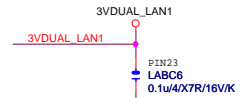
Title		
Size	Document Number	Rev
Custom	H310M S2P 2.0	1.0
Date:	Friday, June 15, 2018	Sheet 36 of 51

LAN:RTL8111HS	R2.02
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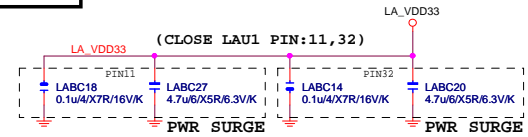


```
LABC2:1U CLOSE PIN22[REALTEK REQ]
```

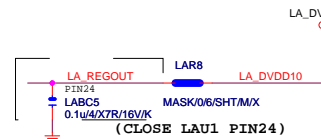
LAN POWER note: lan power連接及電流



(CLOSE LAU1 PIN23)



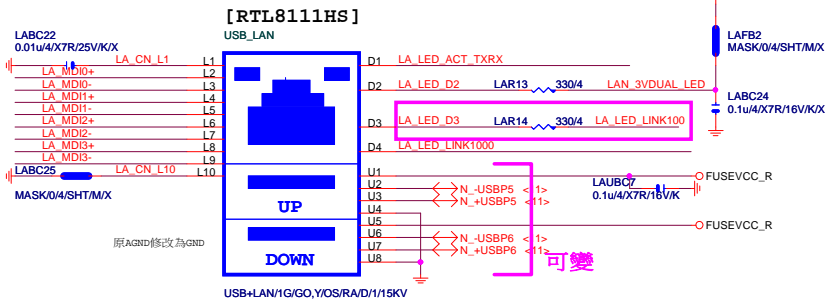
```
LABC18,27:CLOSE PIN11[REALTEK SURGE]
LABC14,20:CLOSE PIN32[REALTEK SURGE]
```



NOTE:
RT8106E:PIN3,11,22,24-->NC
LABC2LABC3,LABC5,LABC18,LABC27-->N/A

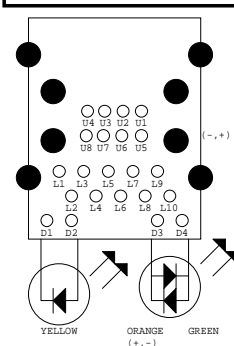
USB_LAN CONNECTOR	R1.06
-------------------	-------

note:可變更USB NAME



LA_MDI-->100歐姆:[20/4/8/4/20]

USB_LAN LAYOUT示意图




Dual Color LED

D4 D3 Green

D4 D3 Orange

Single Color LED

D2  D1 Yellow

LAN POWER note: lan power連接及電流

note: lan power連接及電流

可變

3VDUAL_LAN1

LAPW1

LAPW2

MASK/0/4/SHT/M/X

0/4/X

3VDUAL_PCH

3VDUAL

USB POWER note:可變更FUSE

note:可變更FUSE

可變



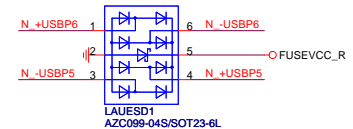
Close to connector

USB_LAN 2-Port 2.0A

FUSE-0805

RMA ESD PROTECT note:可變更USB NAME

note:可變更USB NAME

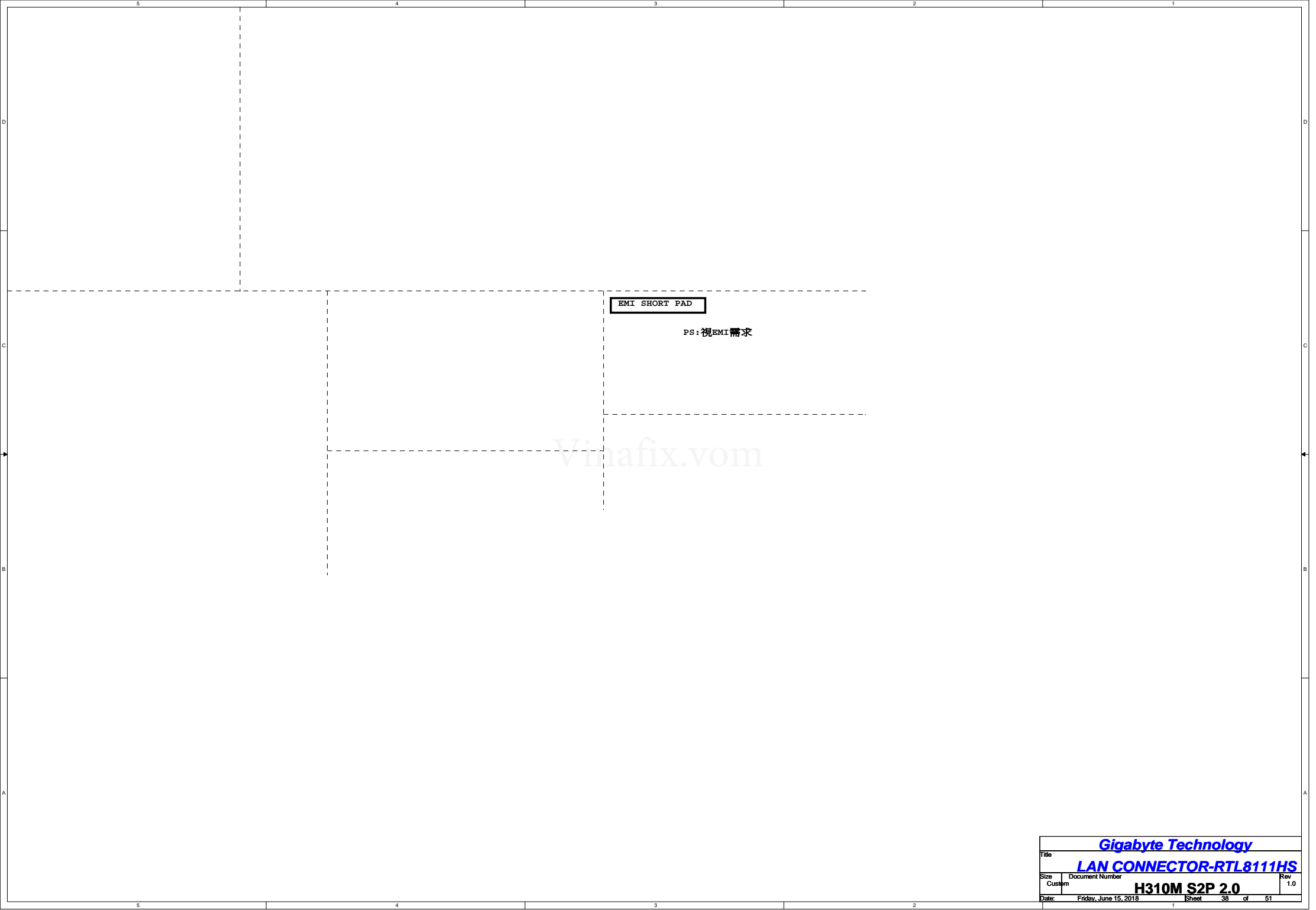


Gigabyte Technology

Realtek RTL8111HS

H310M S2P 2.0

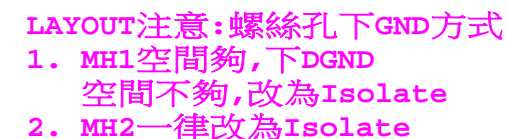
Title			
<p align="center">Sigabyte Technology Realtek RTL8111HS</p>			
Size Custom	Document Number		Rev
	H310M S2P 2.0		1.0
Date:	Friday, June 15, 2018	Sheet	37 of 51



EMI SHORT PAD

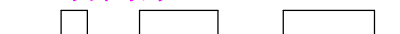
PS:視EMI需求

Vinafix.vom



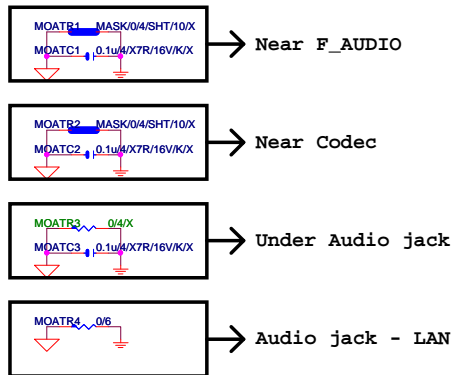
<input type="radio"/> MH1	<input type="radio"/> MH2	
DGND	Isolate	

GND切割線



Rev 2.01

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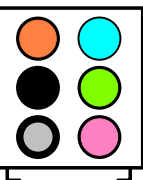


*量産前, 0ohm改short pad

SPDIF_OUT

SPDIF_IN

AZALIA JACK

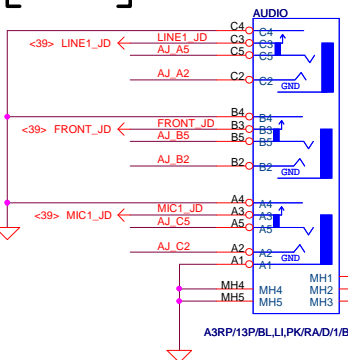


AZALIA JACK

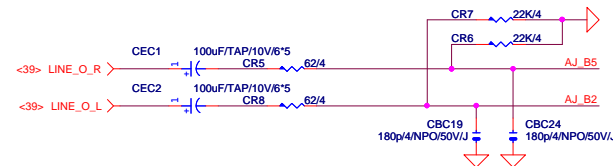
BLUE
LINE-IN

GREEN
LINE-OUT

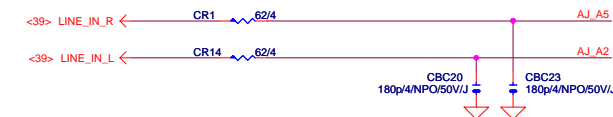
PINK
MIC-IN



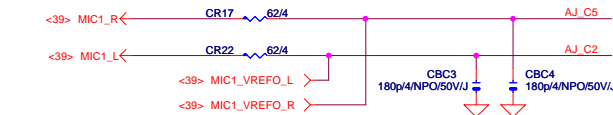
LINE-OUT



LINE-IN



MIC-IN

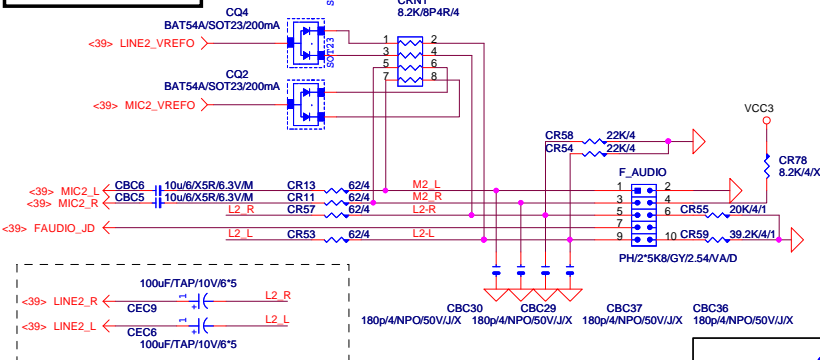


SURROUND

CEN/LFE

SURR BACK

AZALIA FRONT PANEL

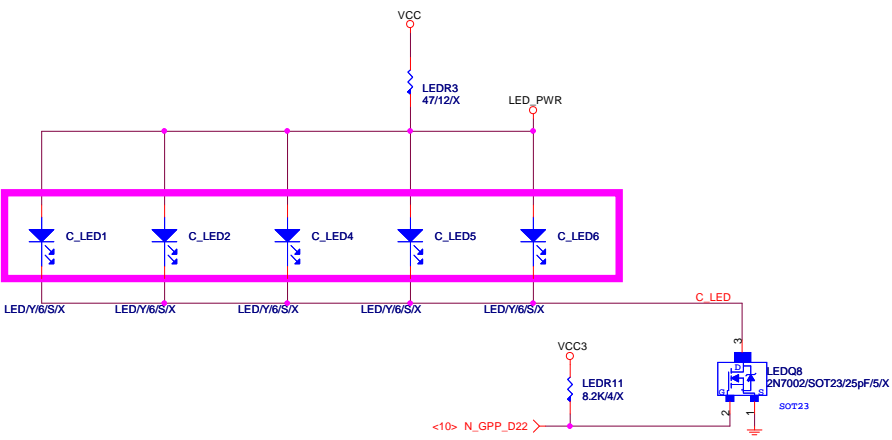


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Title		
AUDIO JACK		
H310M S2P 2.0		
Size	Document Number	Rev
Custom		1.0
Date:	Friday, June 15, 2018	Sheet 40 of 51

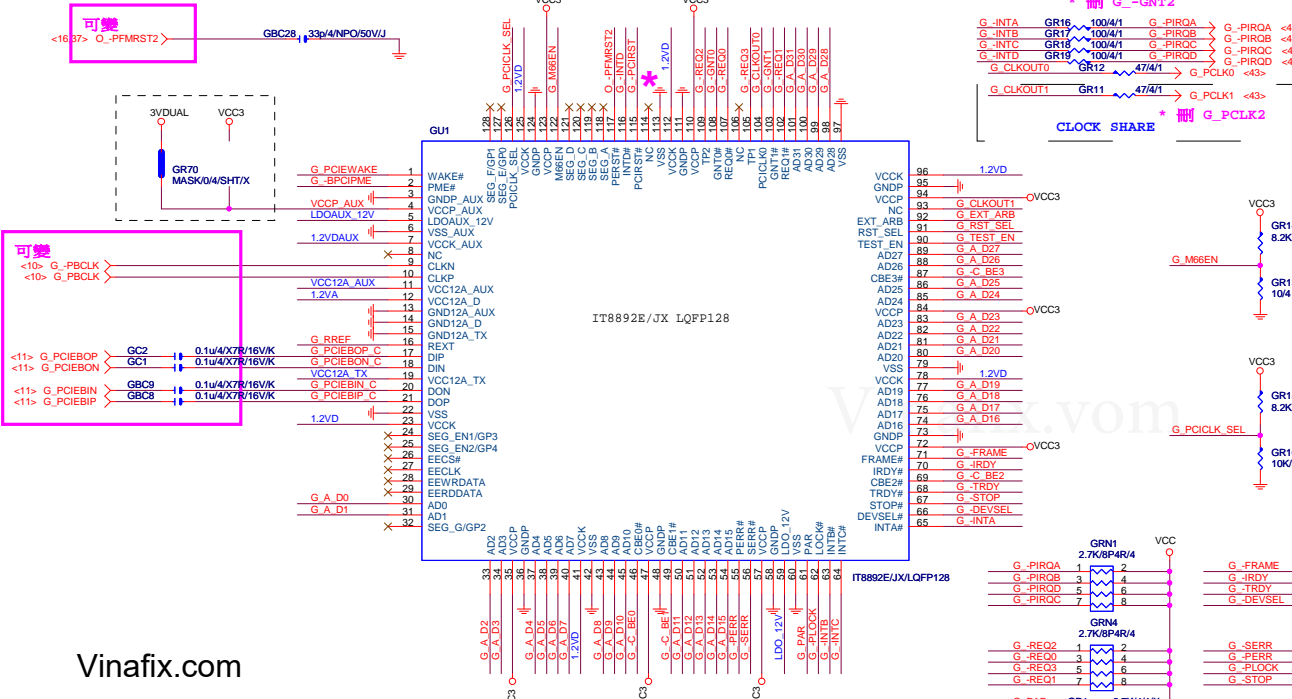
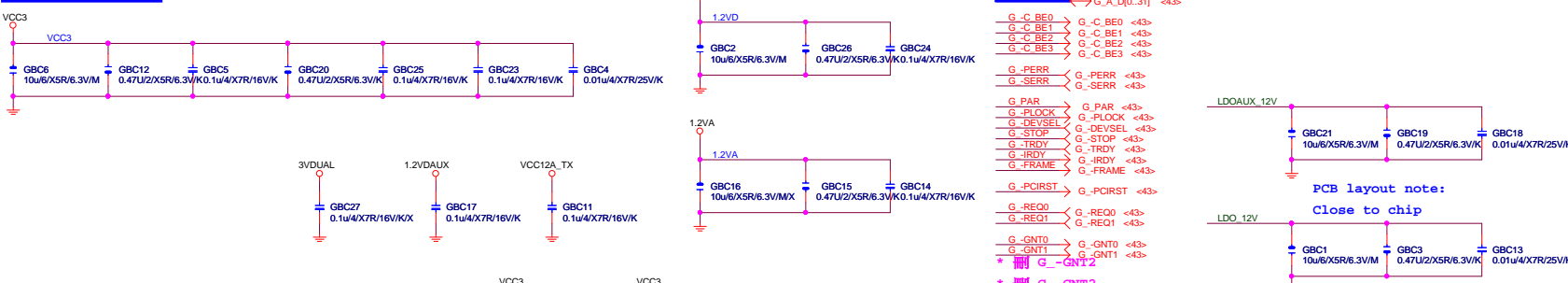
Ambient LED Control

	N_GPP_D22
ill Mode	H
OFF Mode	L



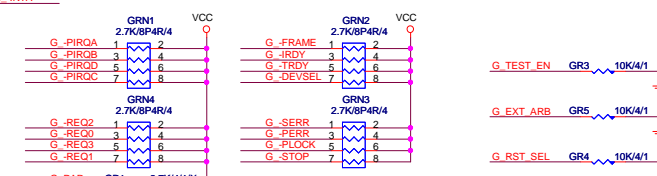
Vinafix.vom

Rev 0.1

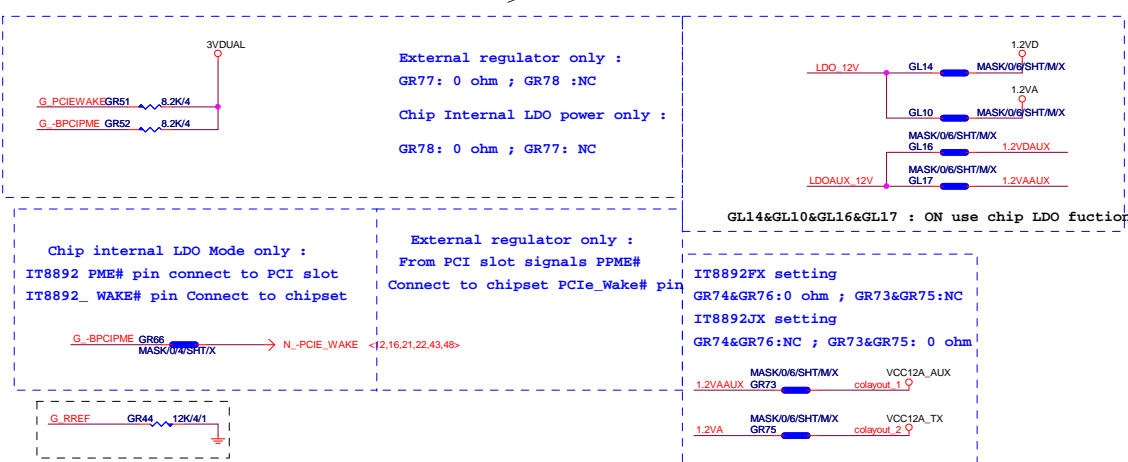


High: Enable PCI CLK 66MHz
Low: Disable PCI CLK 66MHz

High: PCICLK INPUT form CLK Gen
Low: PCICLK OUTPUT form IT8893 chip




Vinafix.com



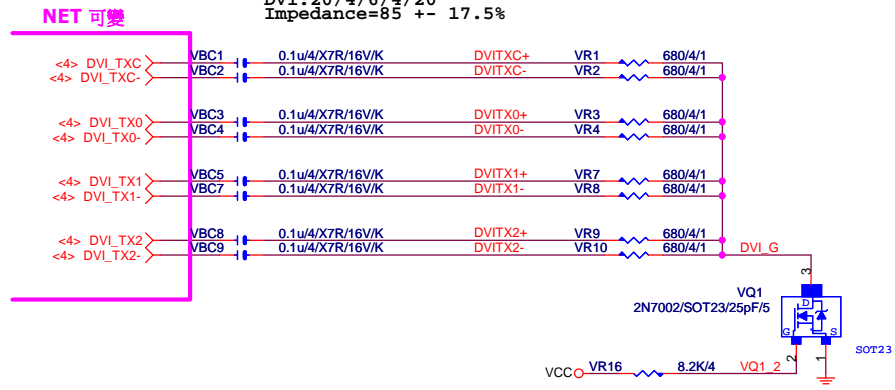
	Component change note
IT8892FX	GR70,GR74,GR76,GR78,GR66 : ON GR69,GR73,GR75,GR77,GR67 : NC GR44 resistor is 12k ohm GL14,GL10,GL16,GL17 : ON GL19,GL21,GL23,GL25: NC
IT8892JX	GR70,GR73,GR75,GR78,GR66 : ON GR69,GR74,GR76,GR77,GR67 : NC GR44 resistor is 18k ohm GL14,GL10,GL16,GL17 : ON GL19,GL21,GL23,GL25: NC
External LDO Power (IT8892JX)	GR69,GR73,GR75,GR77,GR67 : ON GR70,GR78,GR66 : NC GR44 resistor is 18k ohm GL19,GL21,GL23,GL25 : ON GL14,GL10,GL16,GL17 : ON

Gigabyte Technology

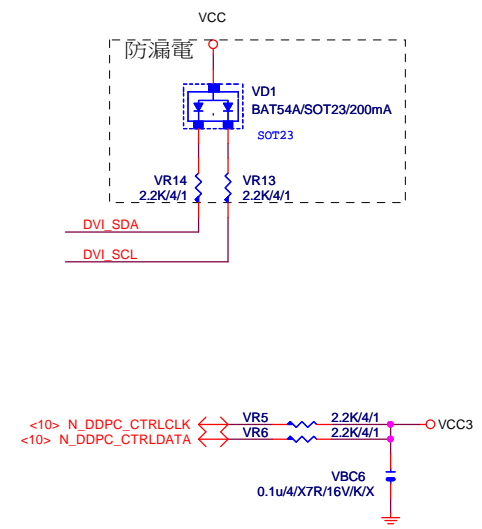
<div style="text-align: center;">  </div>			
Title			
R_USB30,USB_OC			
Size	Document Number	Rev	
Custom	H310M S2P 2.0	1.0	
Date:	Fri, Jun 15, 2018	Sheet	42 of 51

DVI

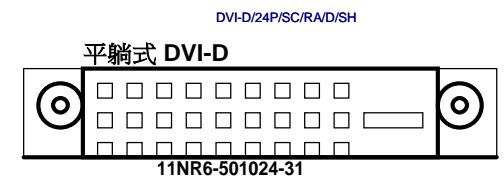
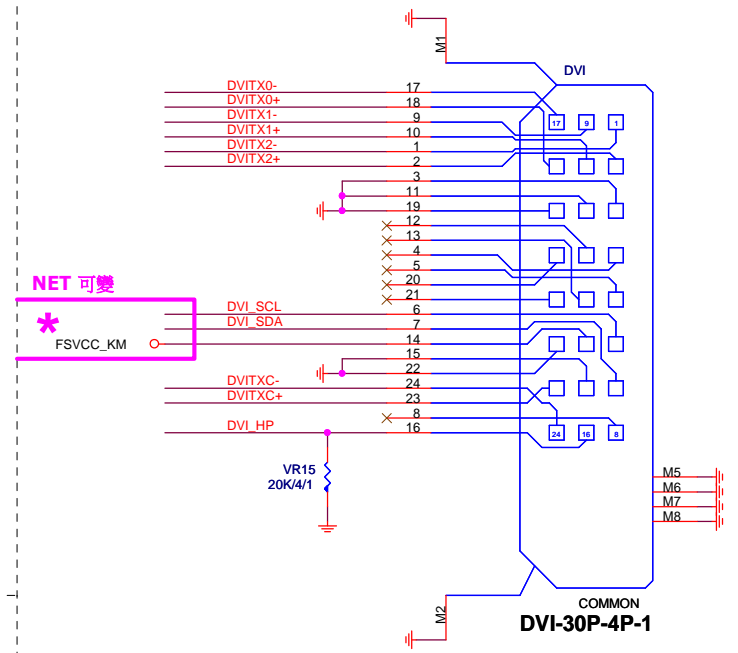
DVI: 20/4/6/4/20
Impedance=85 +- 17.5%



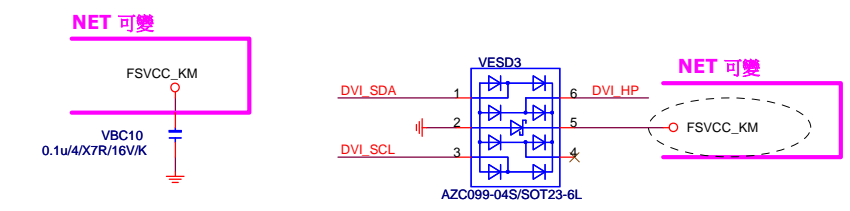
DVI PU



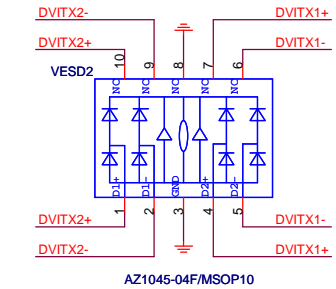
DVI CONN



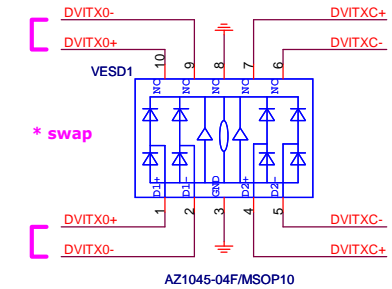
ESD



Close to connector



Close to connector

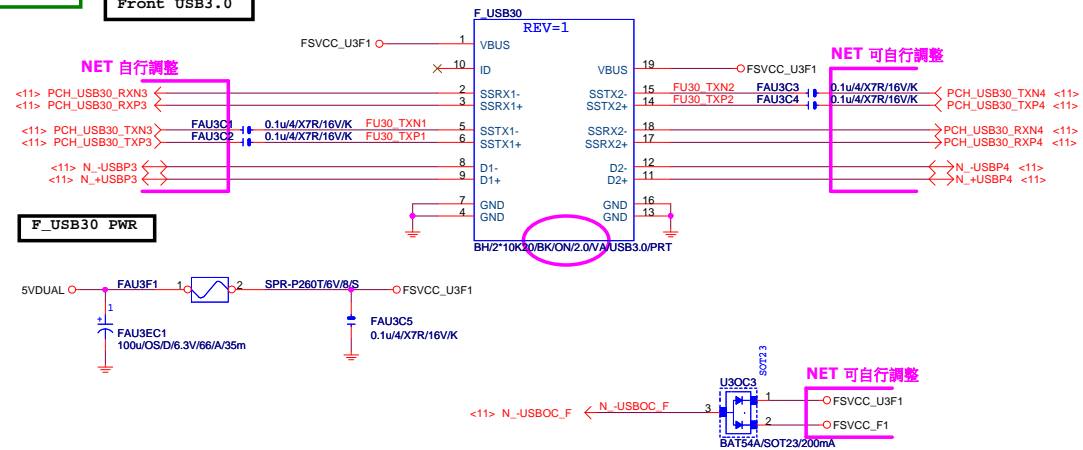


Close to connector

GIGABYTE			
Title			
DVI			
Size	Document Number	H310M S2P 2.0	
Custom		Rev 1.0	
Date:	Friday, June 15, 2018	Sheet	44 of 51

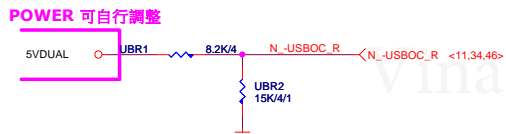
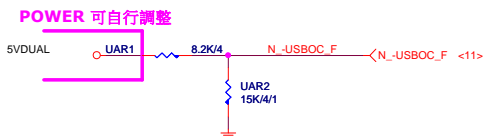
Rev: 0.7

Front USB3.0



-USB0C_F

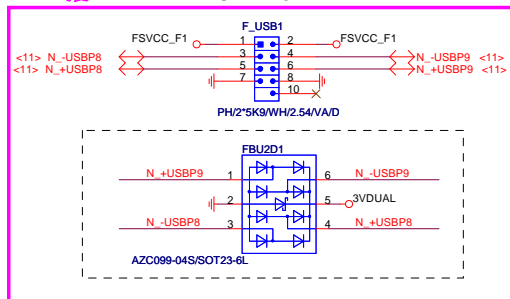
-USB0C_R



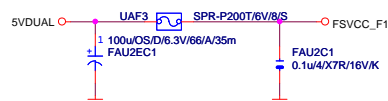
Rev: 0.7

FRONT USB1

NET 可變 FUSB2X5-HS

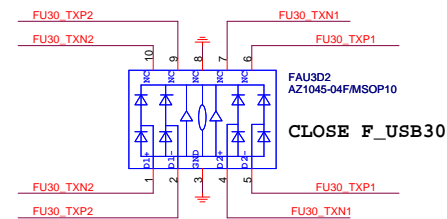
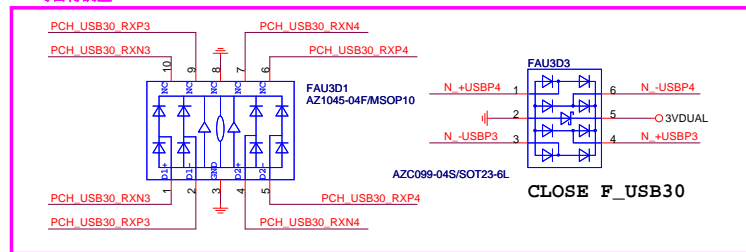


Close to connector
FUSE 2 Port 1 Fuse 2A

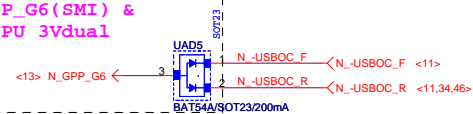


Close to connector
FUSE 2 Port 1 Fuse 2A

NET 可自行調整

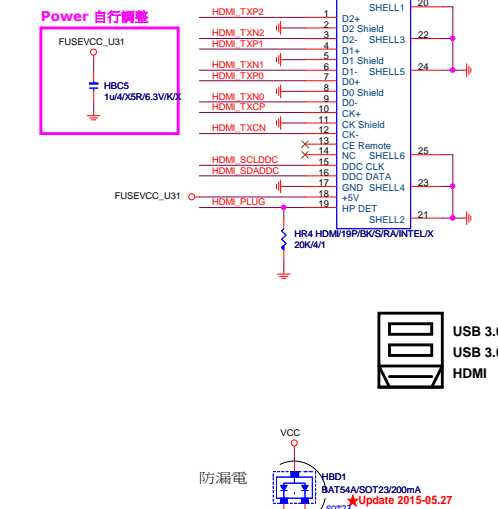
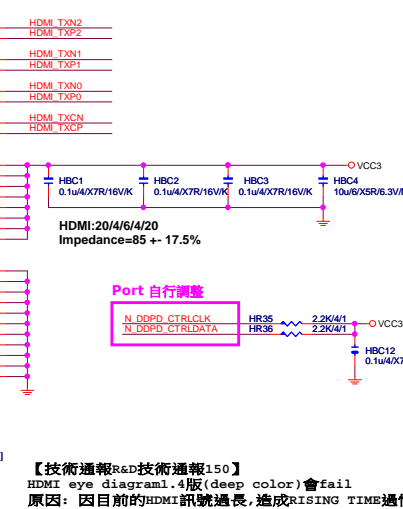
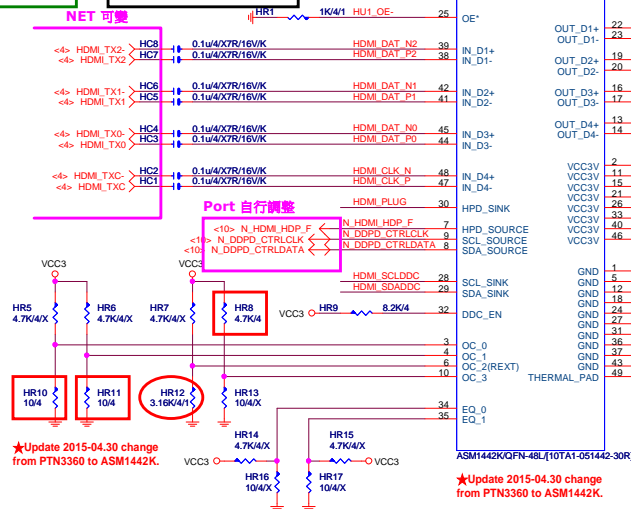


```
* 接 PCH
N_GPP_G6(SMI) &
PCH PU 3Vdual
```



Rev: 0.4

HDMI LEVEL SHIFT



PTN3360:PIN 4/10/34/35 NC PIN,都不上值;只上HR12:10K
ASMI442:紅色框要上,HR12:3.16K

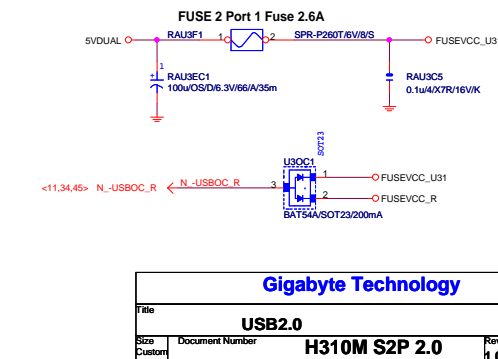
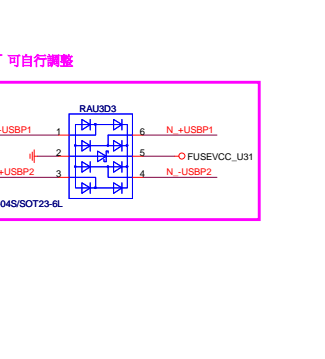
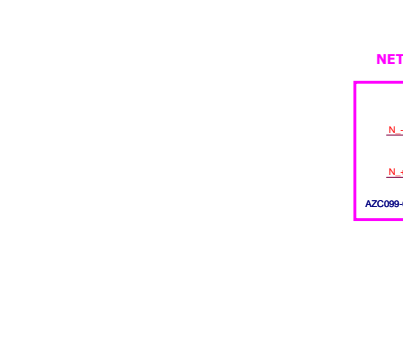
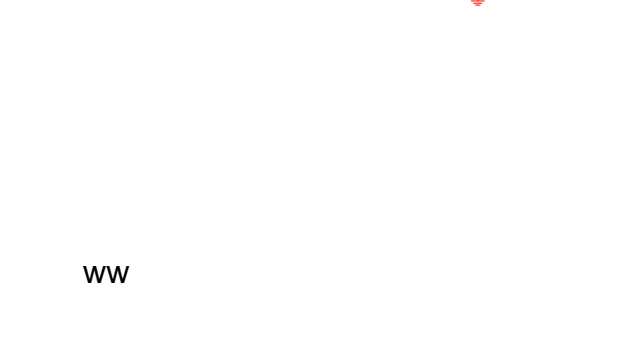
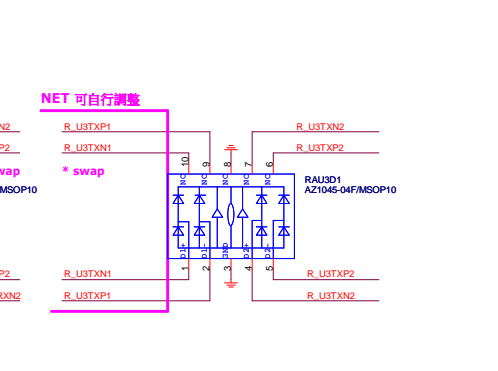
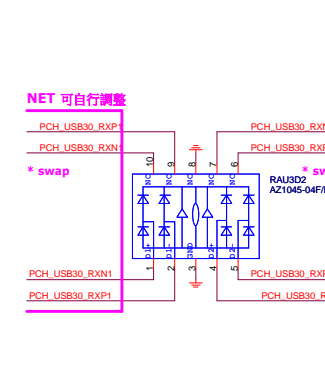
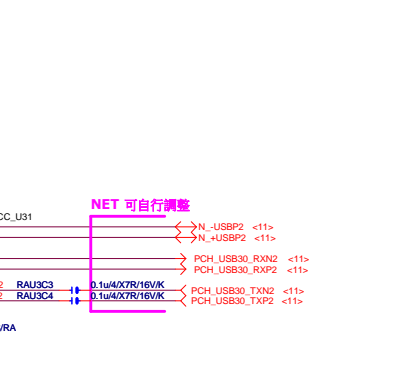
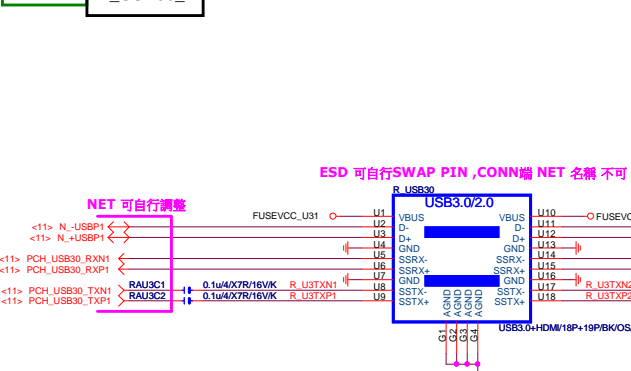
【技術通報R&D技術通報150】
HDMI eye diagram1.4版(deep color)會fail
原因: 因目前的HDMI訊號過長,造成RISING TIME過慢,而會壓到eye diagram
改善: ASMEDIA ASMI442 : 3.16K(PIN6 PULL DOWN電阻) 10ohm(PIN4 PULL DOWN電阻)

ASMI442:紅色框要上,HR12:3.16K

ASMI442:紅色框要上,HR12:3.16K

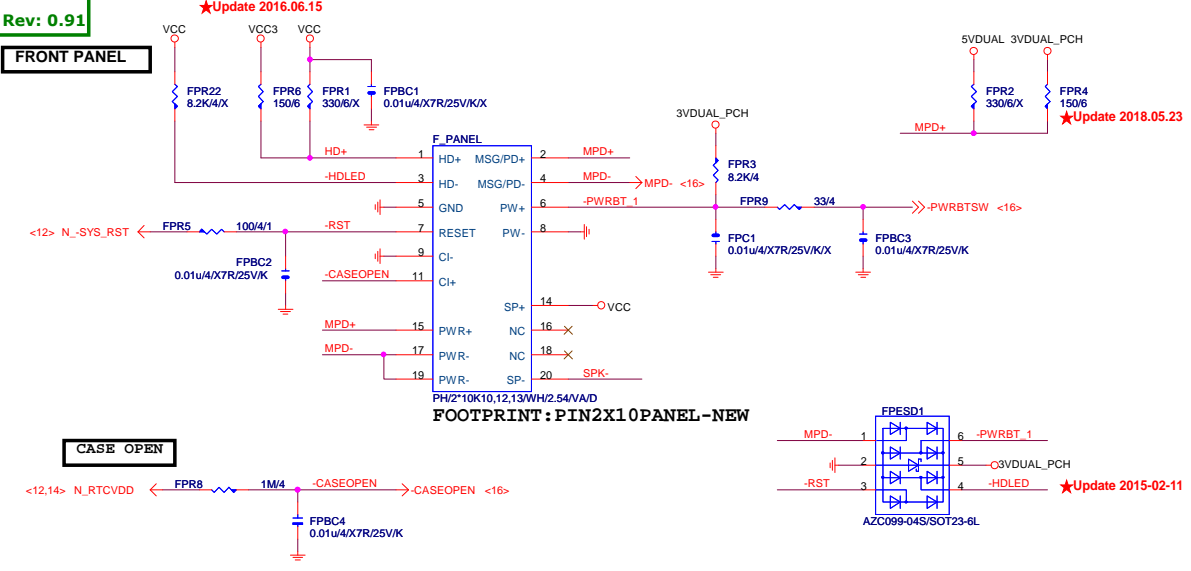
Rev: 0.53

R_USB30_1

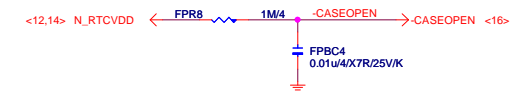


WW

FRONT PANEL

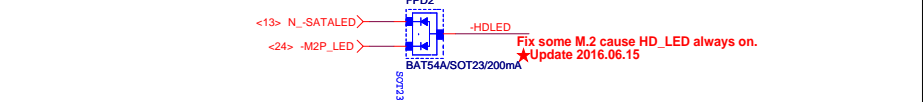


CASE OPEN

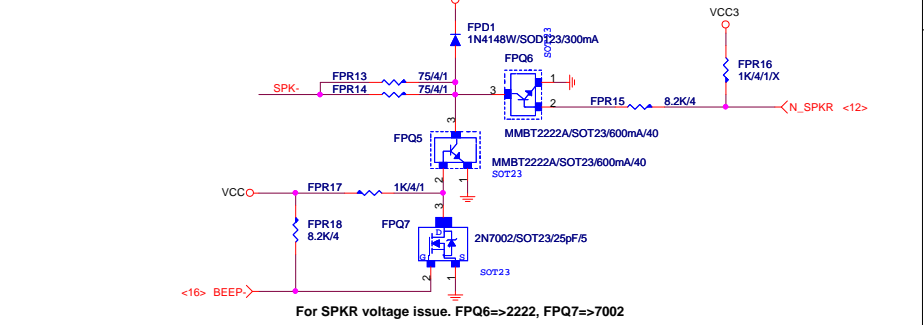


FRONT PANEL SHORT

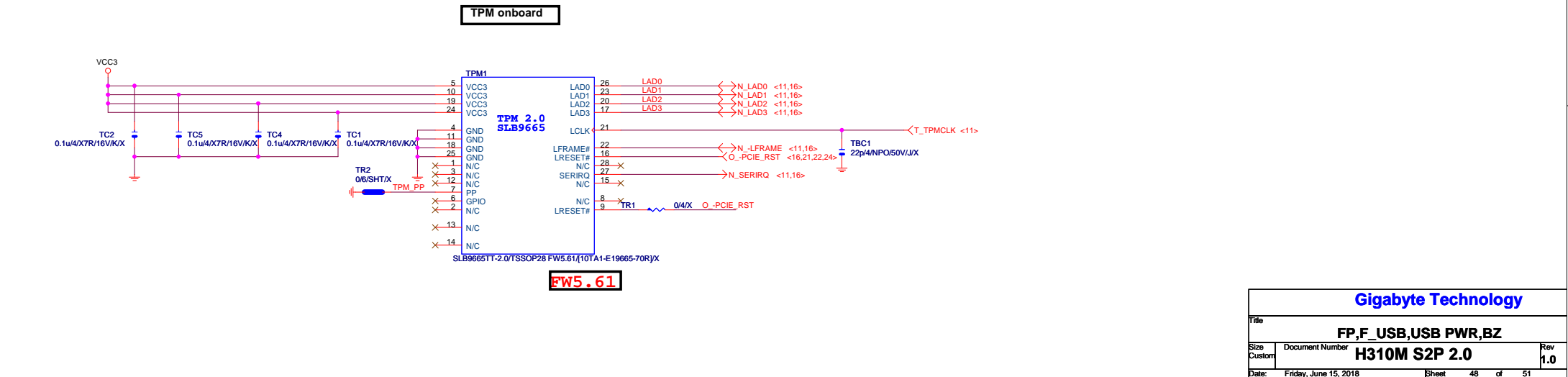
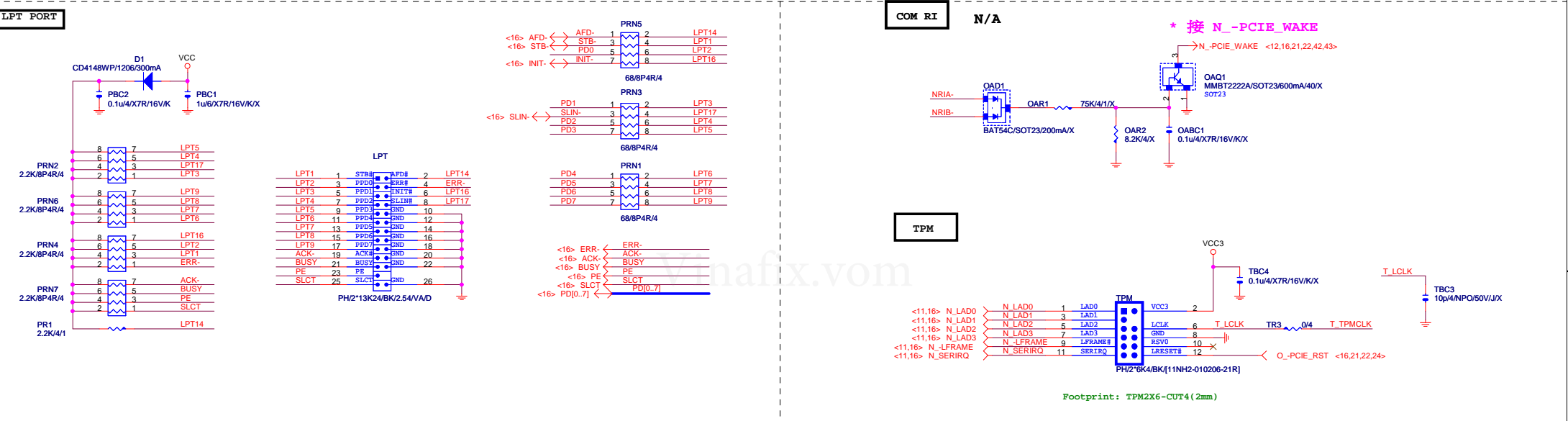
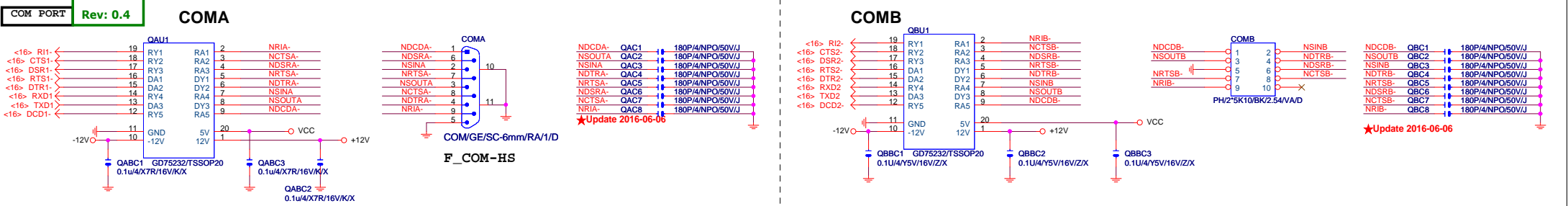
SATA/M.2 LED



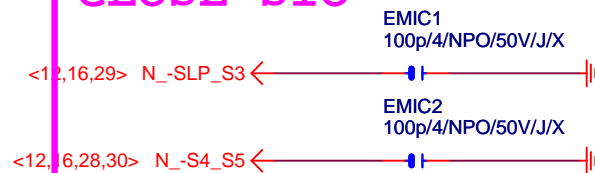
SPKR



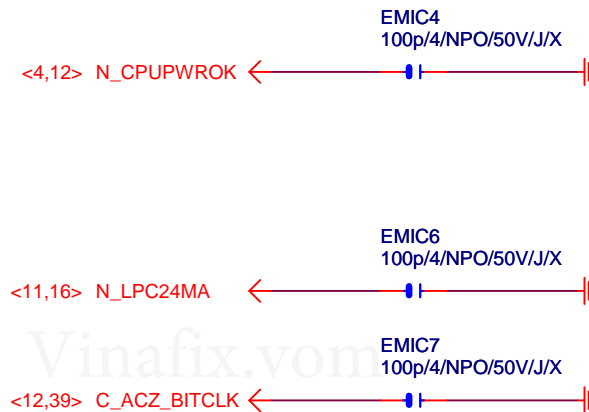
Vinafix.vom



CLOSE SIO



CLOSE PCH



GIGABYTE™

Title

EMI/ESD

Size
A

Document Number

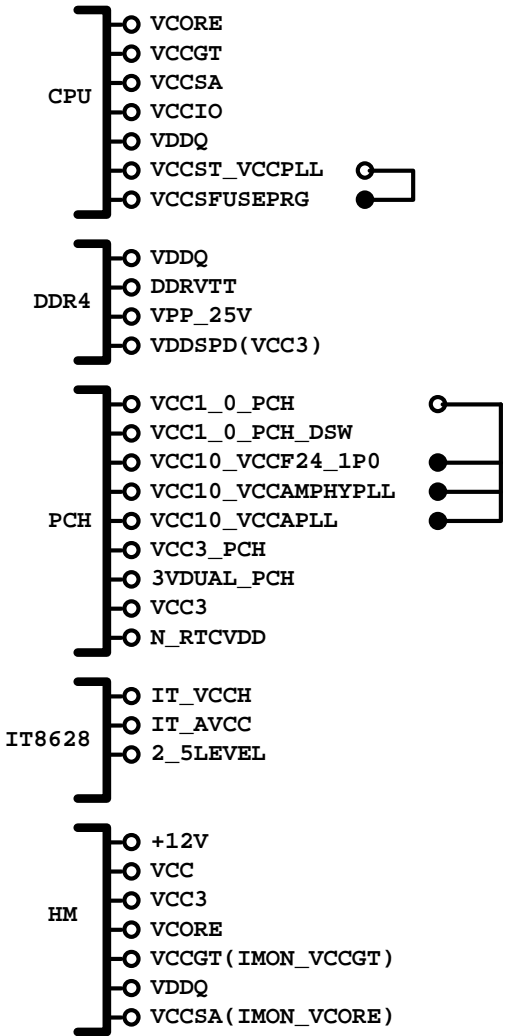
H310M S2P 2.0

Rev
1.0

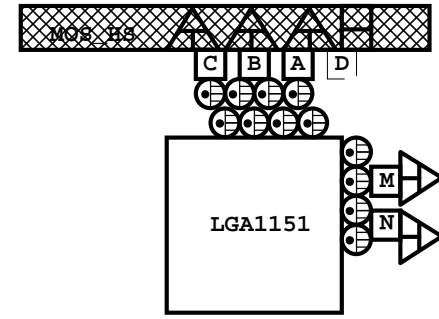
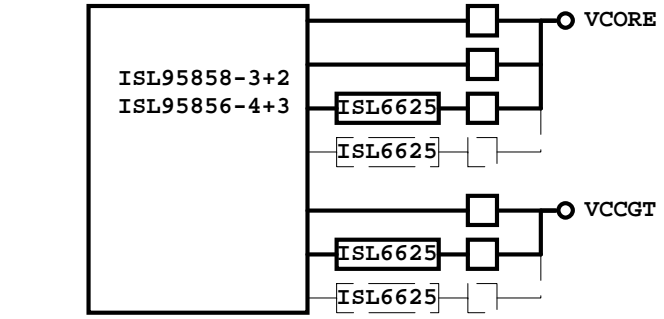
Date: Friday, June 15, 2018

Sheet 49 of 51

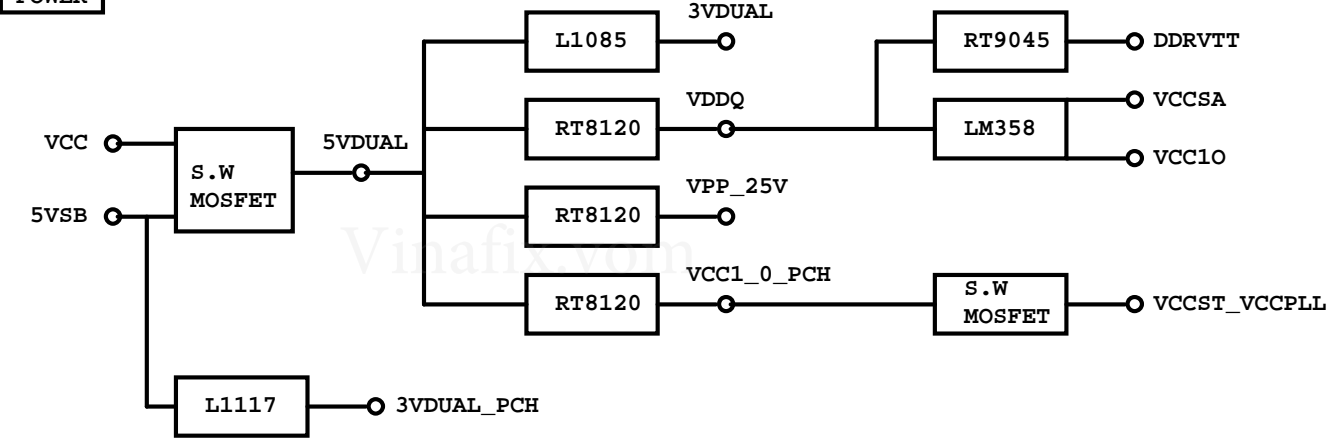
POWER BLOCK MAP



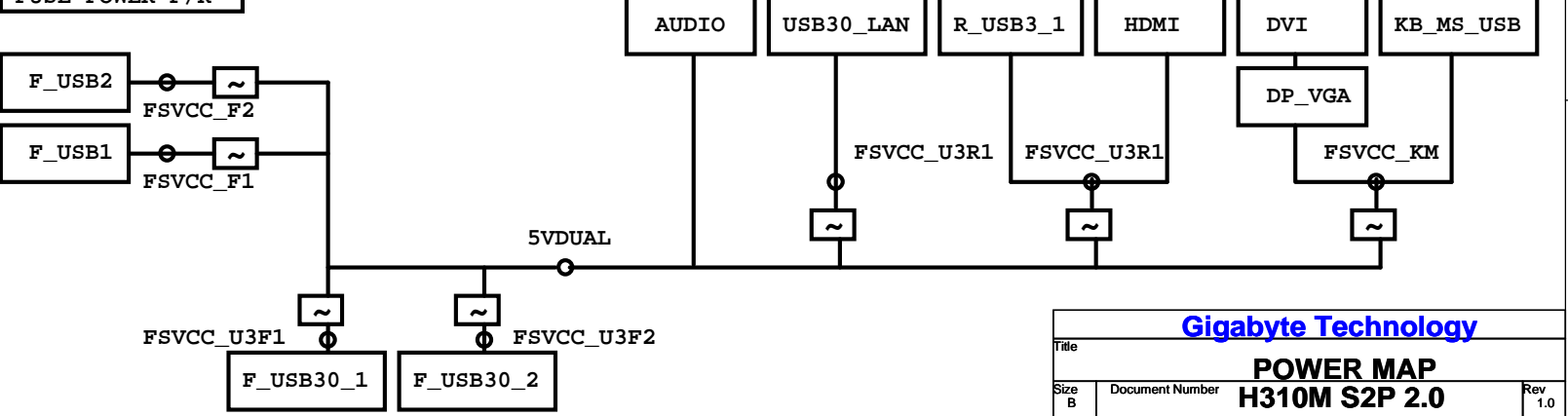
VCORE/VCCGT

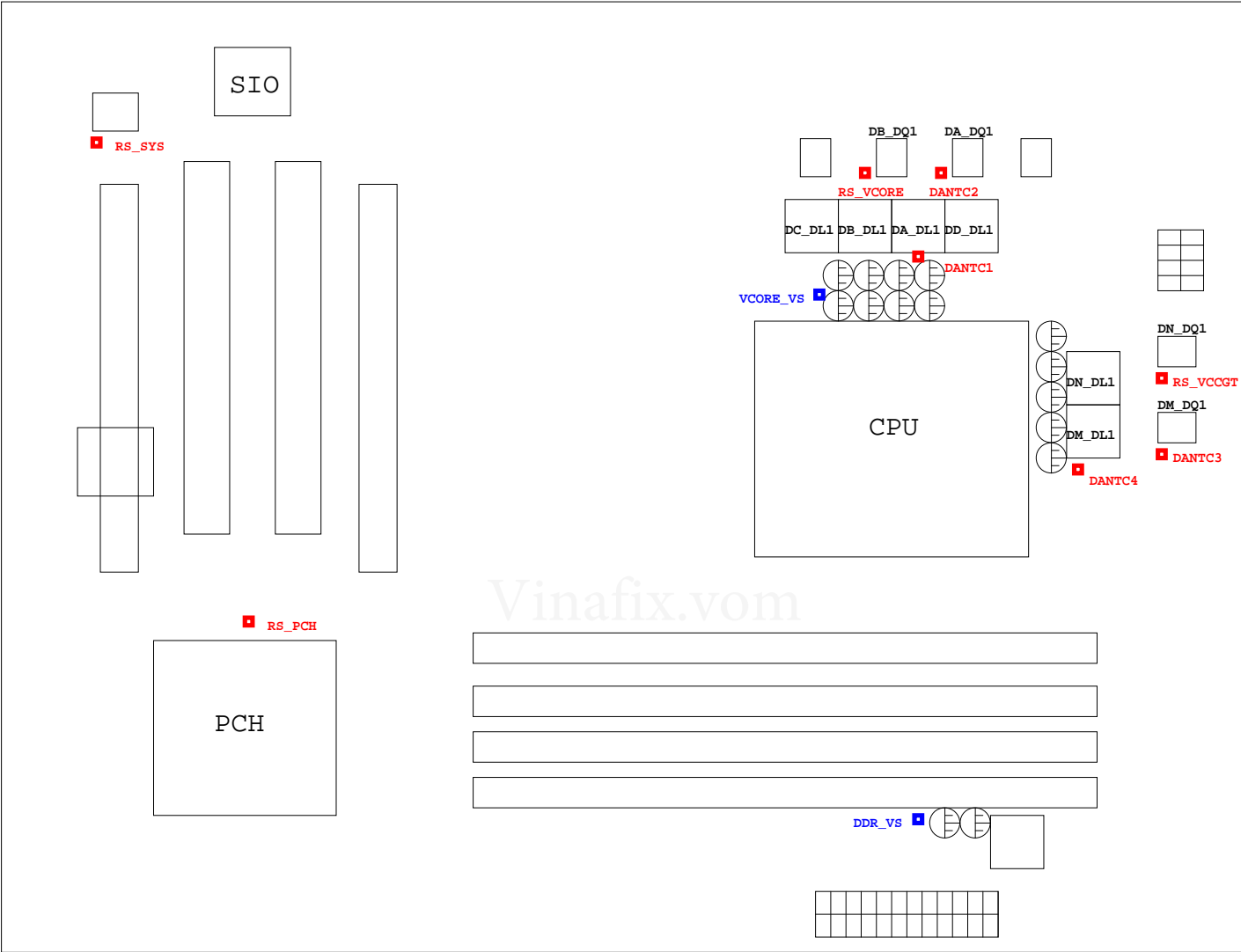


POWER



FUSE POWER F/R





熱敏電阻	擺放靠近位置	走線方式
DANTC1	DA_DL1	N/A
DANTC2	DA_DQ1	Differential
DANTC3	DM_DQ1	N/A
DANTC4	DM_DL1	Differential
RS_VCORE	DB_DQ1	N/A
RS_VCCGT	DN_DQ1	N/A
RS_PCH	PCH	N/A
RS_SYS	CU1	N/A